

FIG.1

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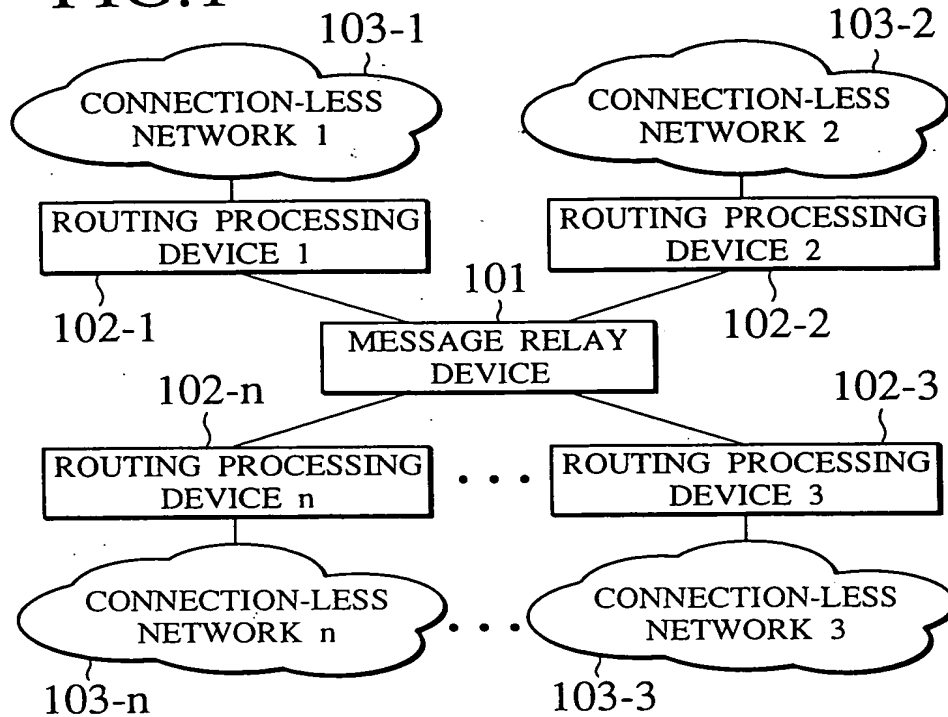
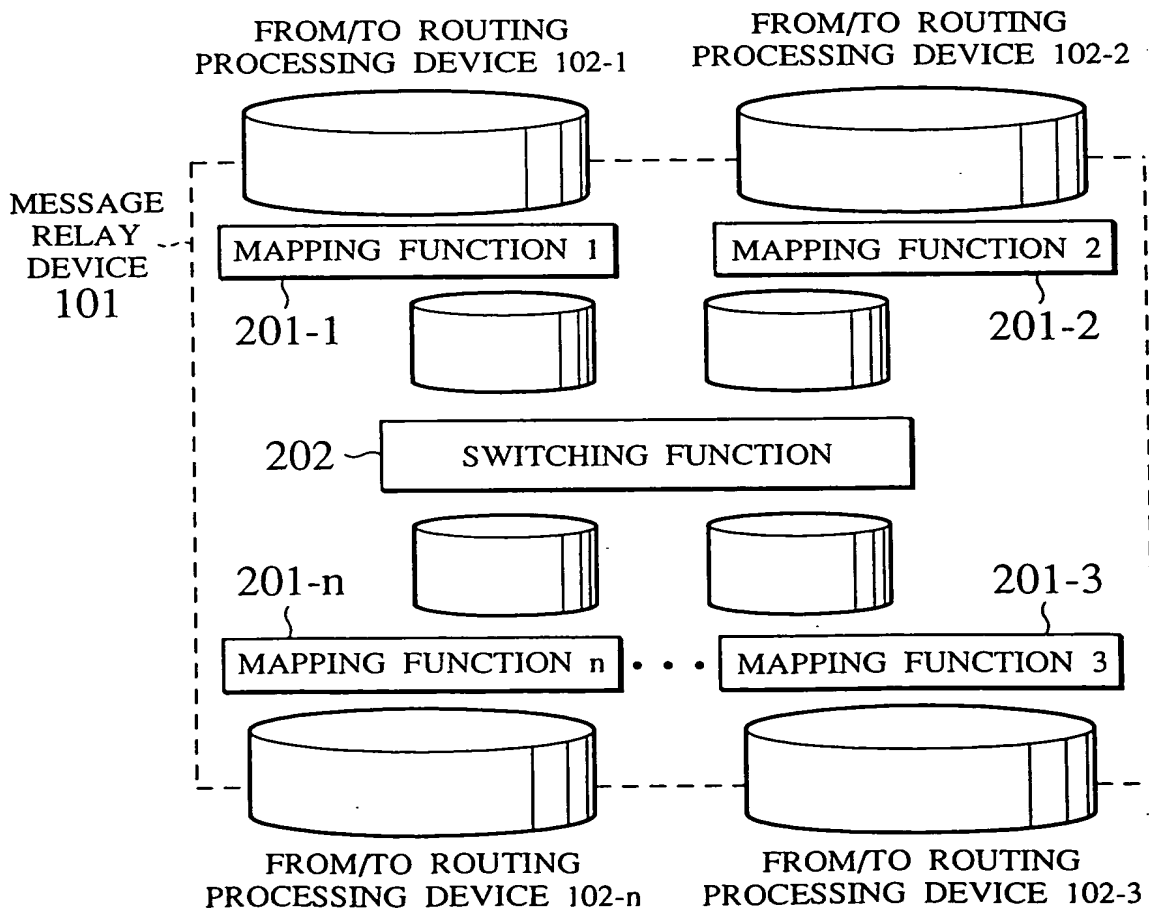
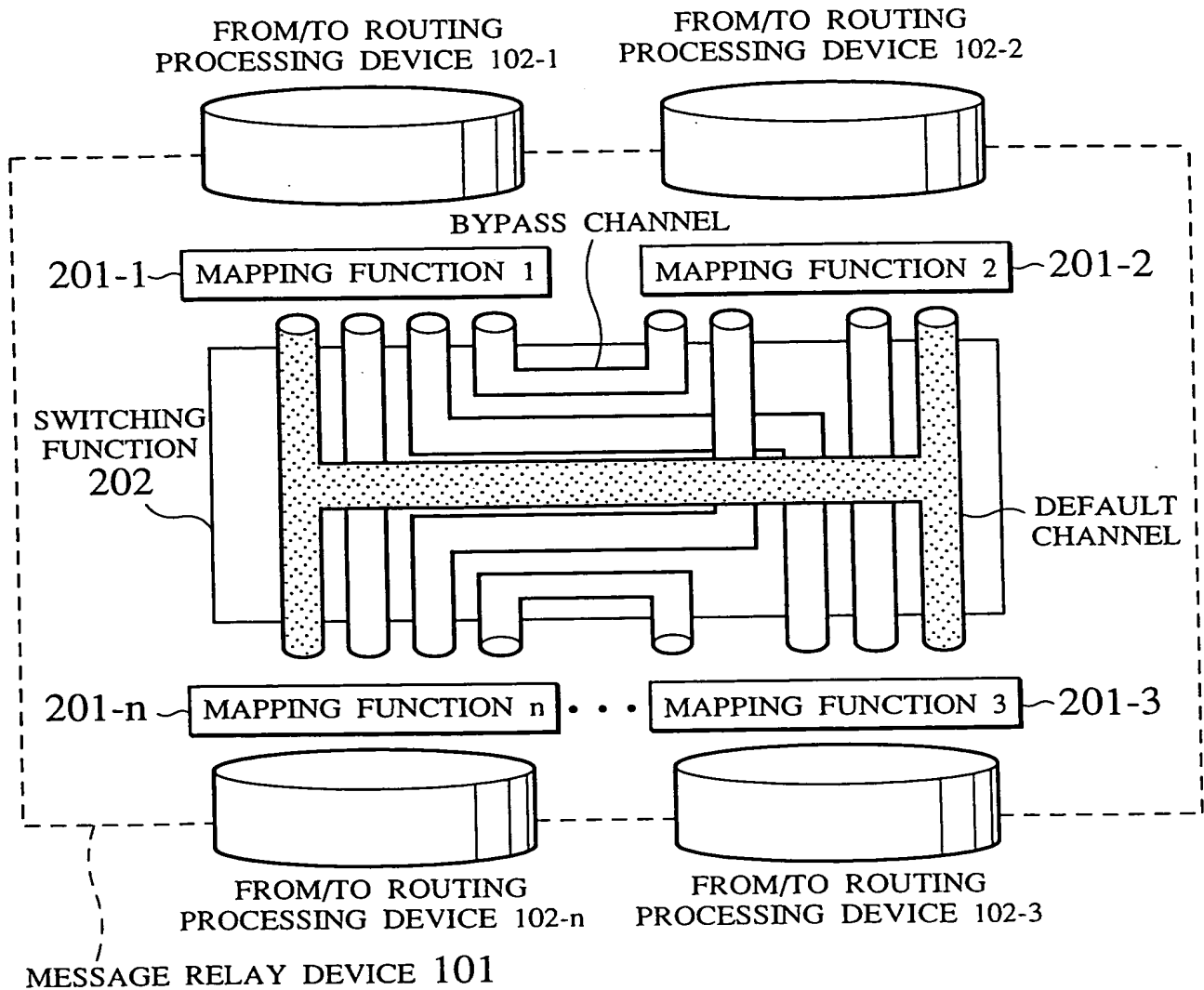


FIG.2



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FIG.3



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FIG.4

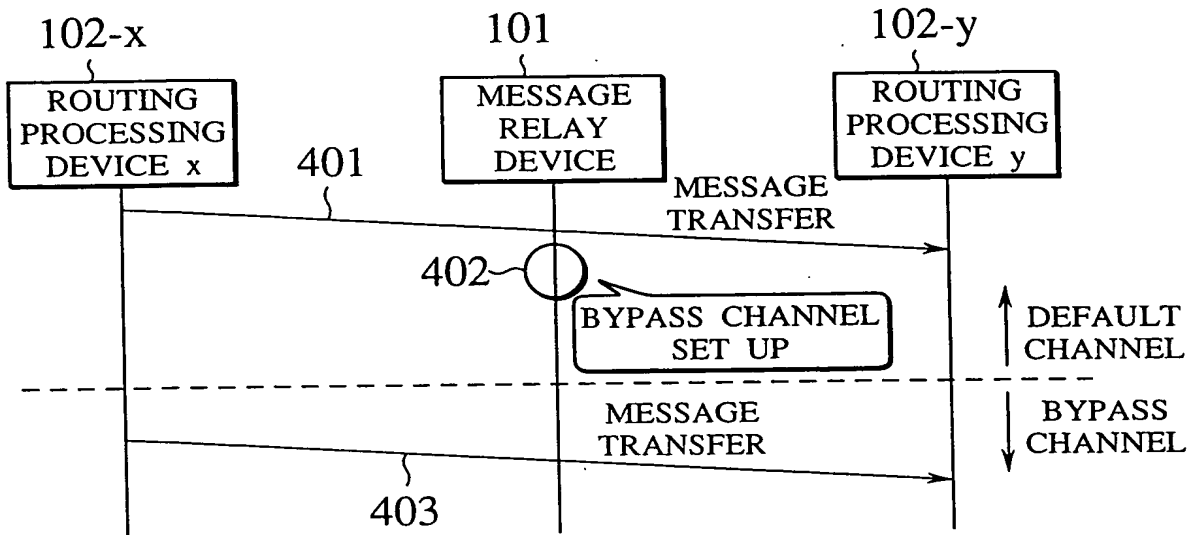
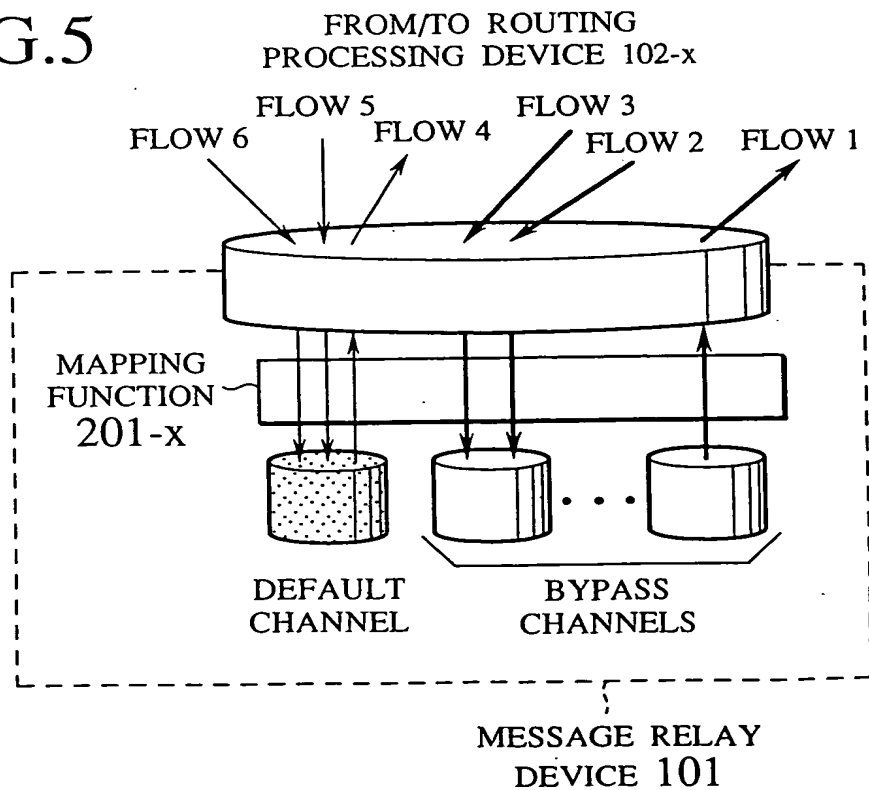
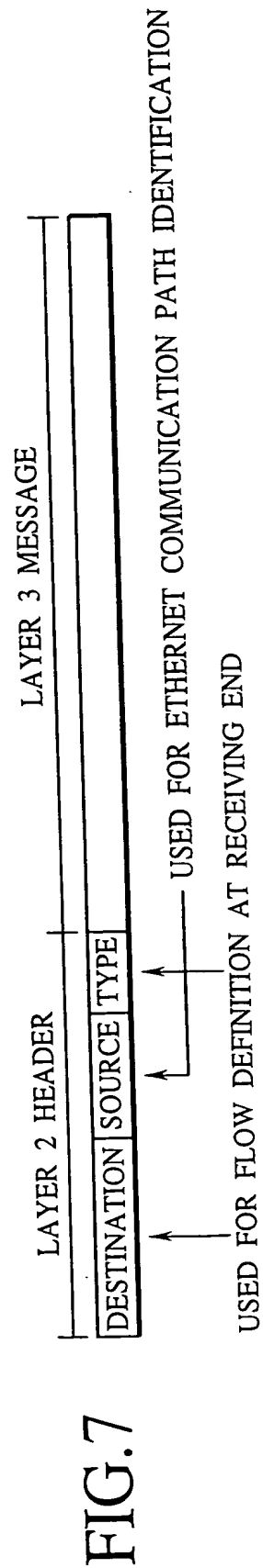
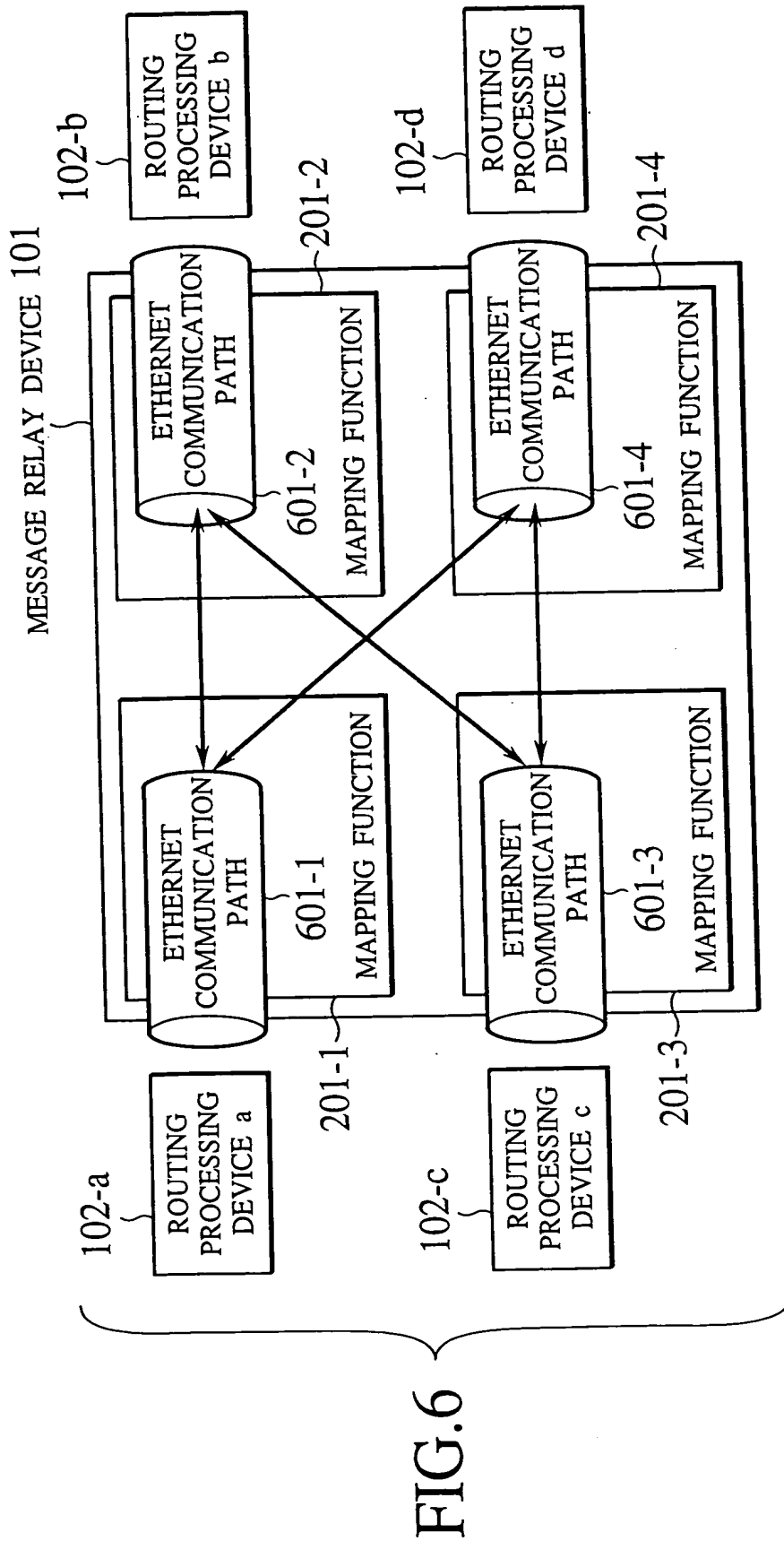


FIG.5

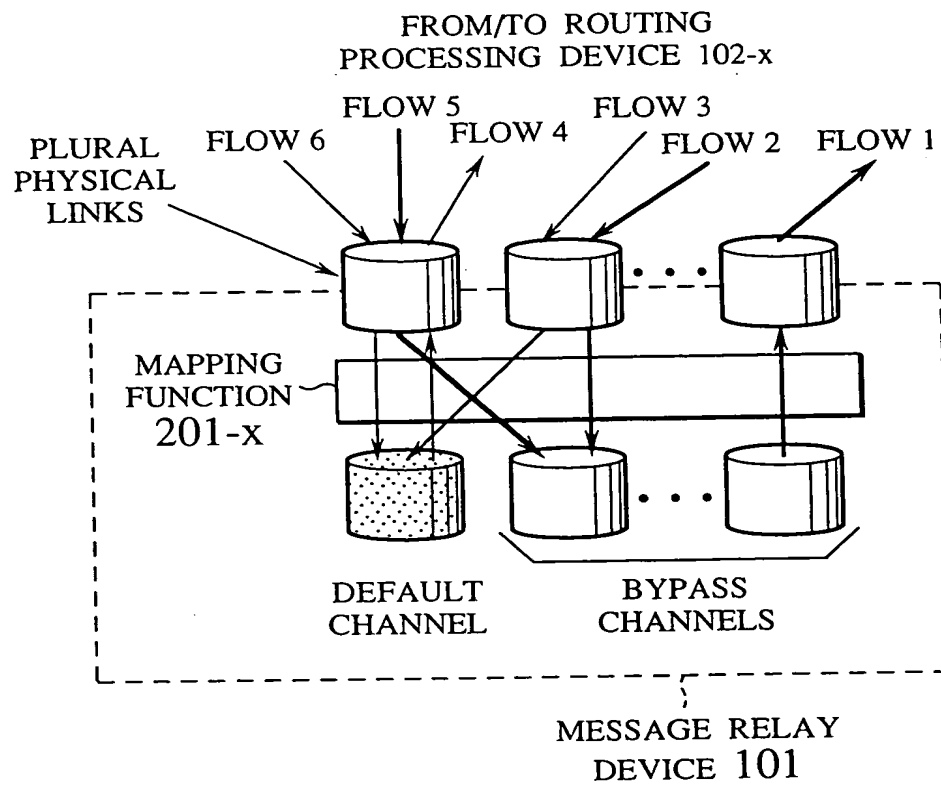


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FIG.8



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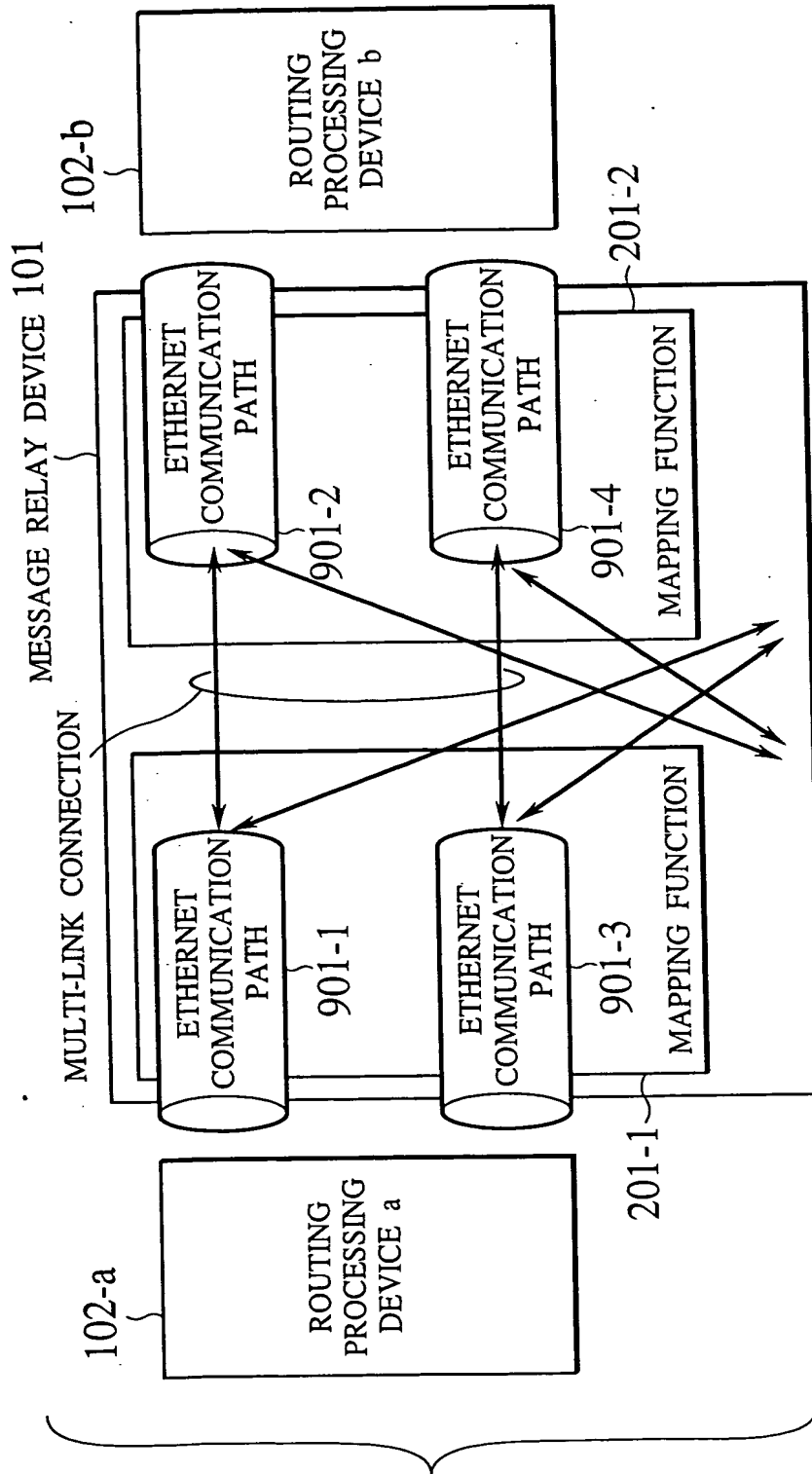


FIG. 9

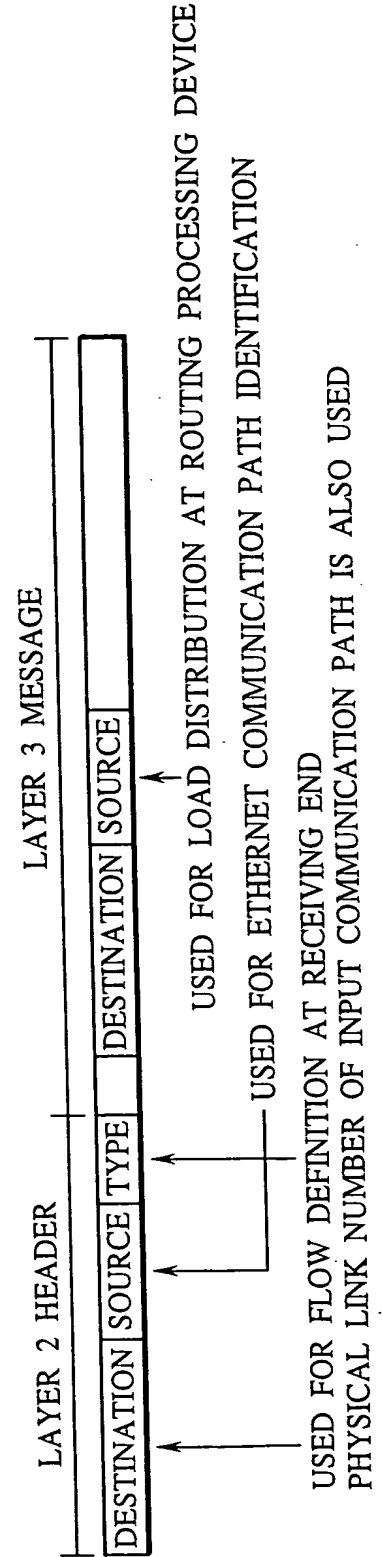
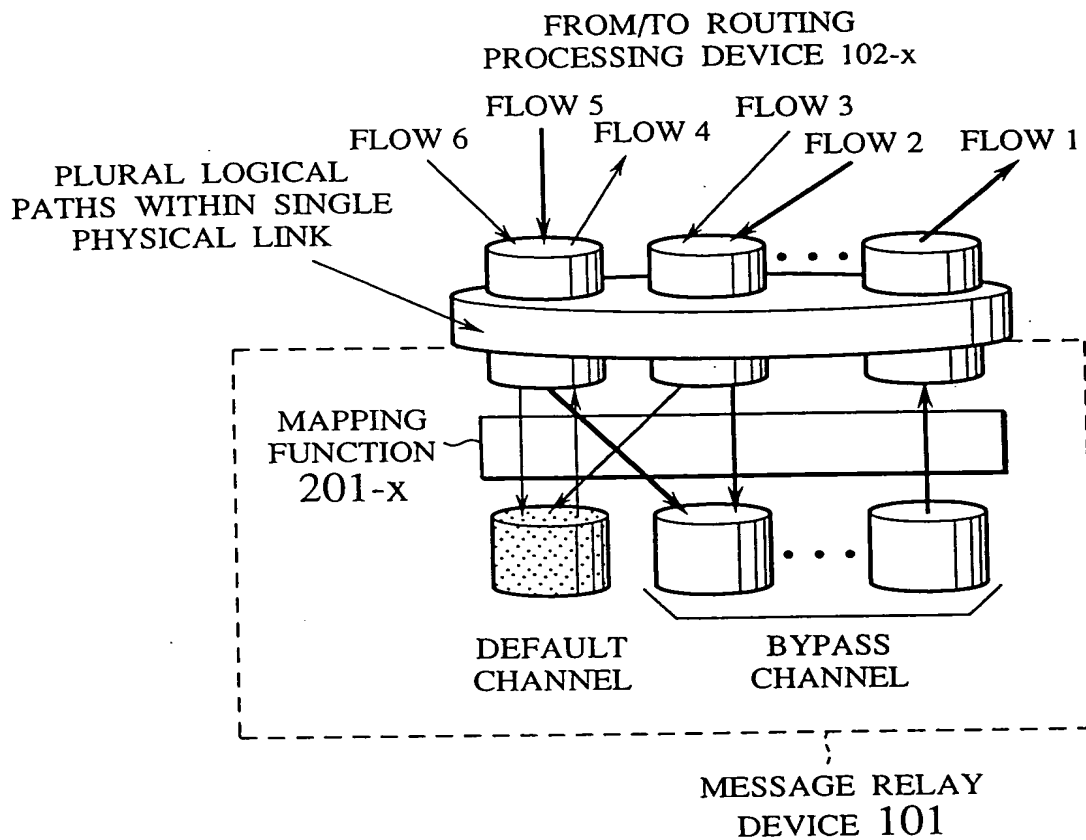


FIG. 10

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FIG.11



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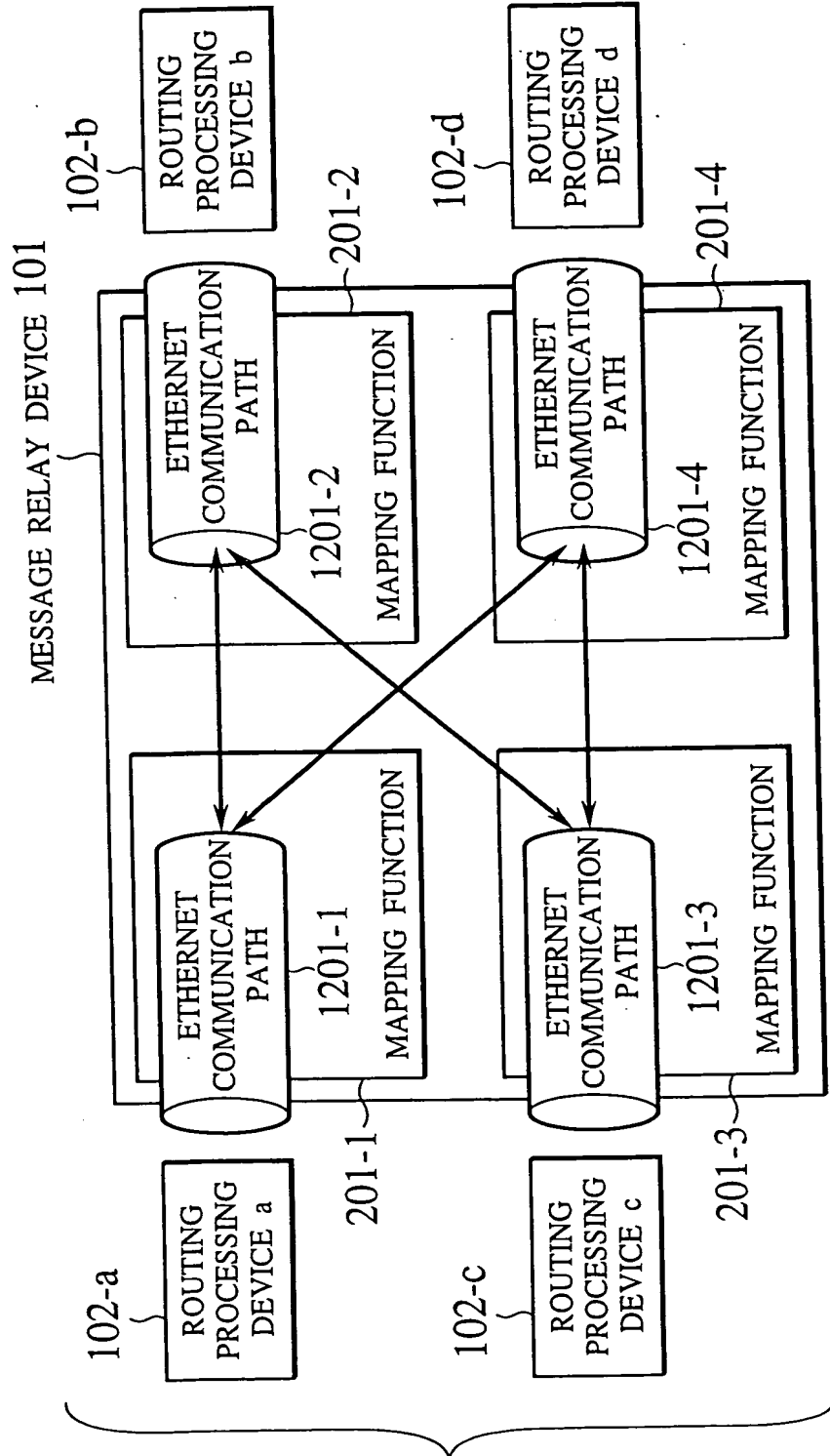


FIG. 12

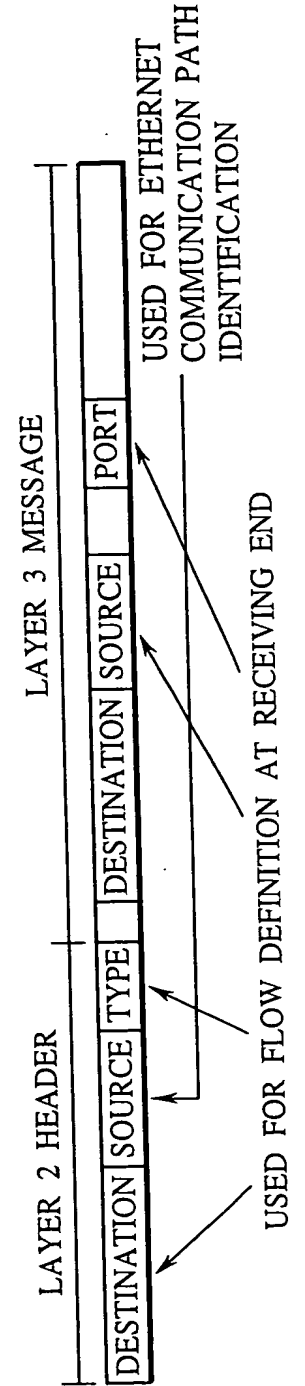
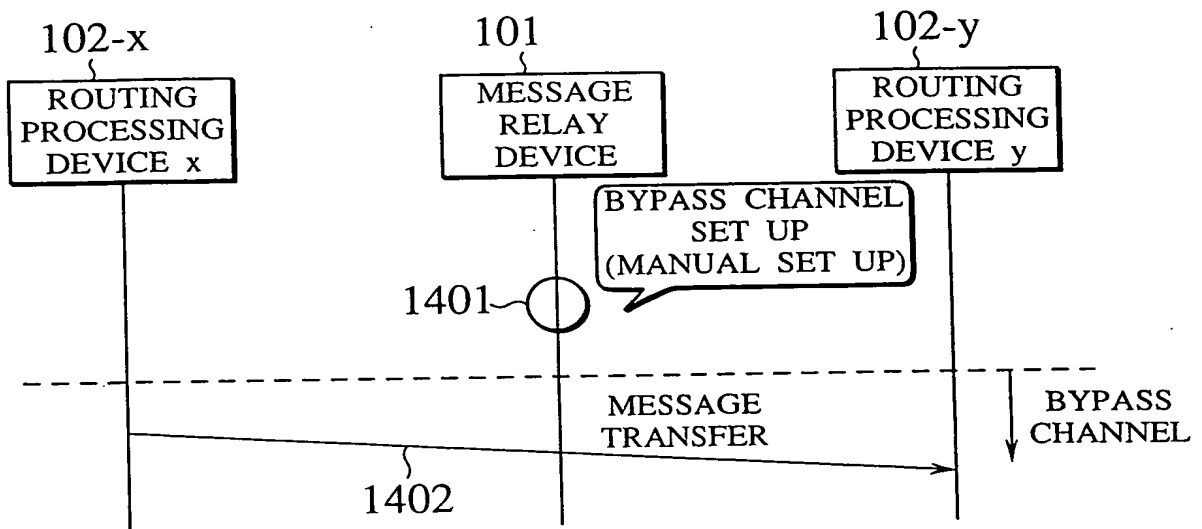
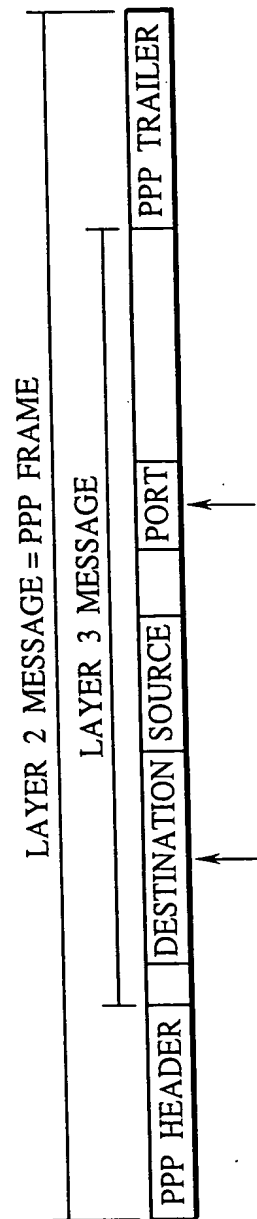
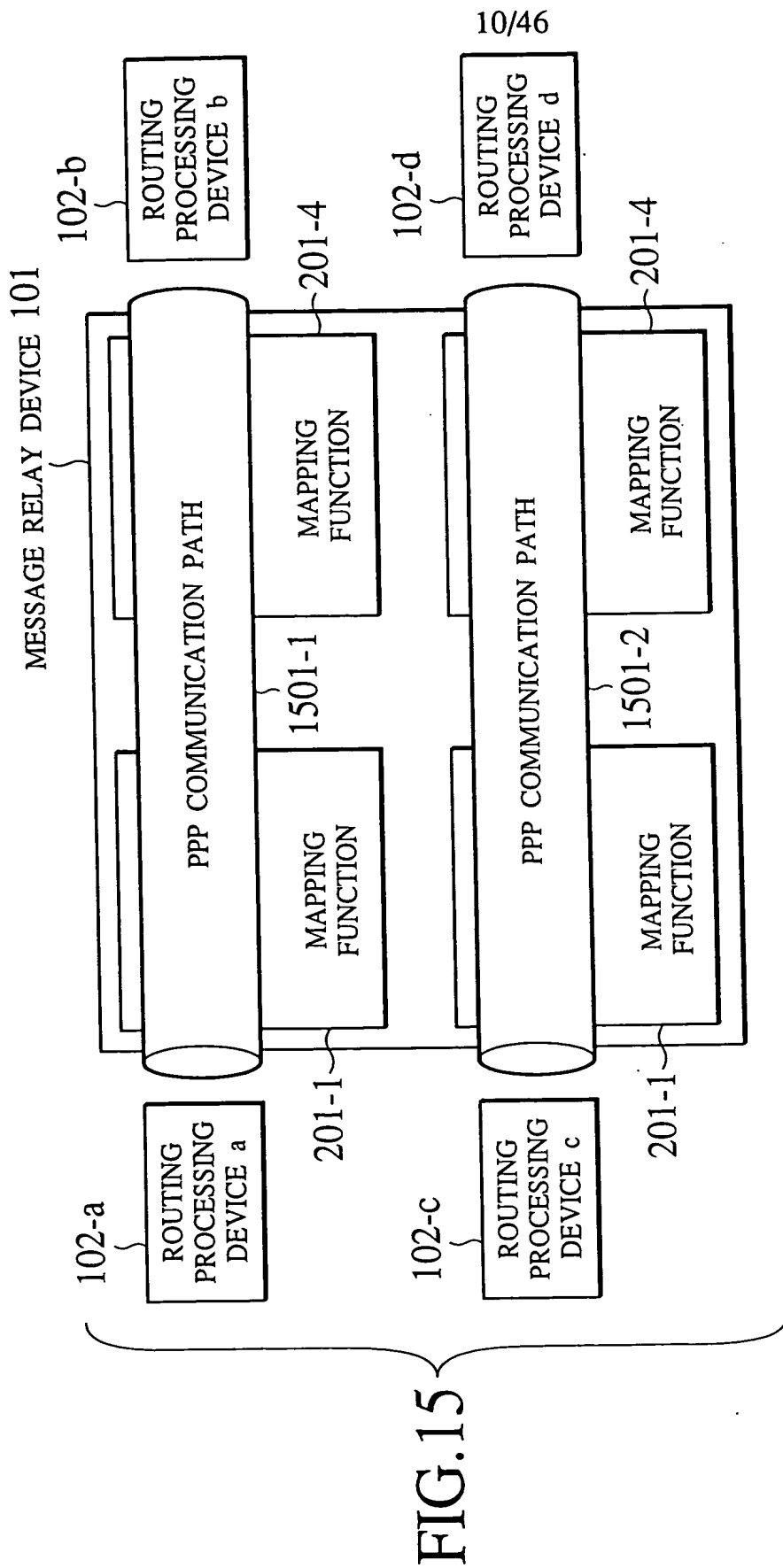


FIG. 13

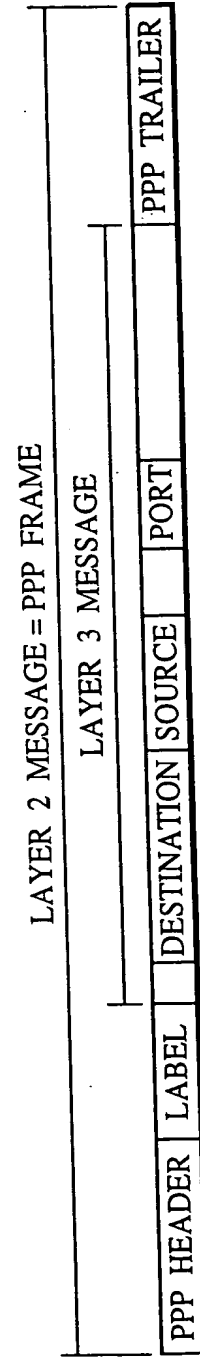
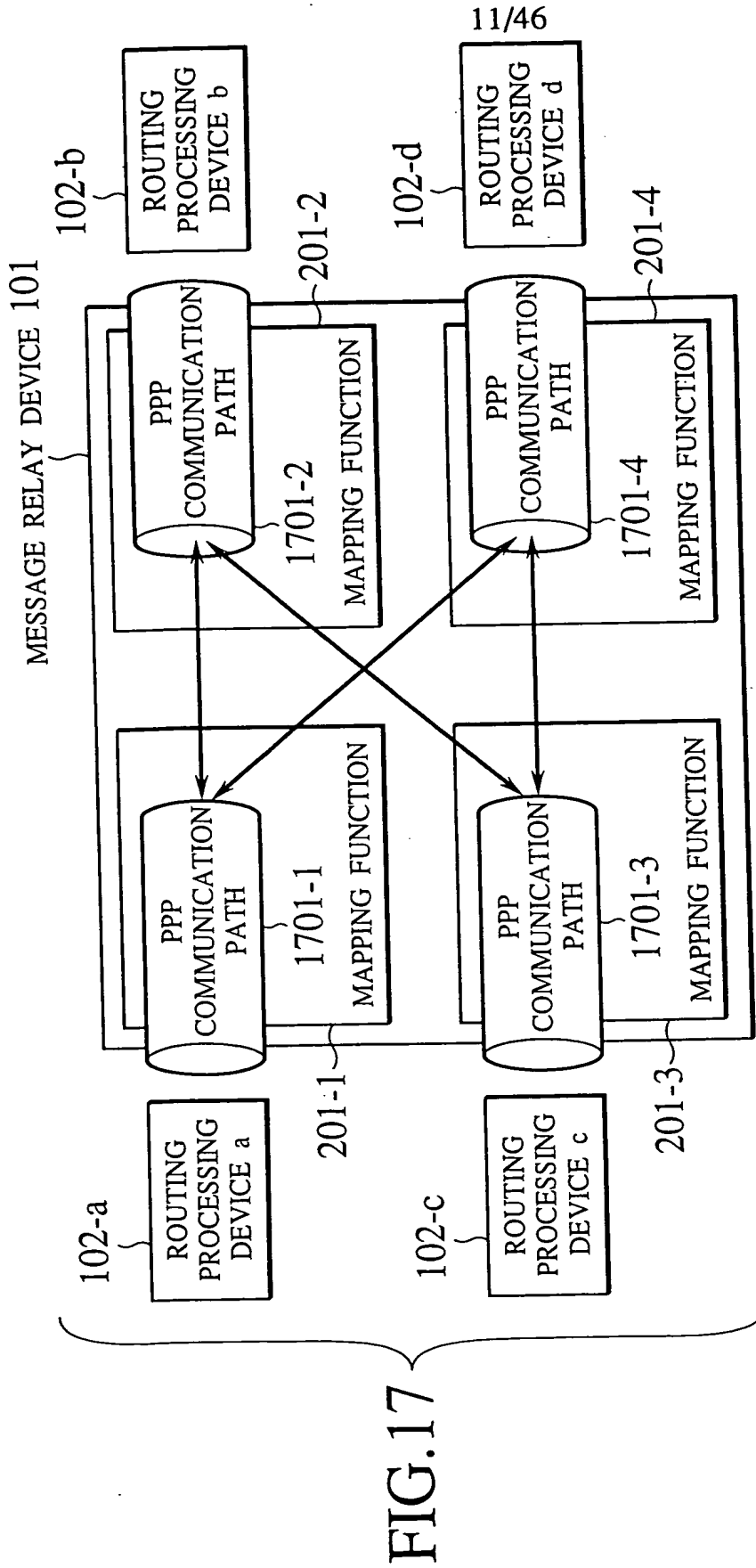
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FIG.14





USED FOR FLOW DEFINITION AT RECEIVING END IN ADDITION
 TO PHYSICAL LINK NUMBER OF INPUT COMMUNICATION PATH



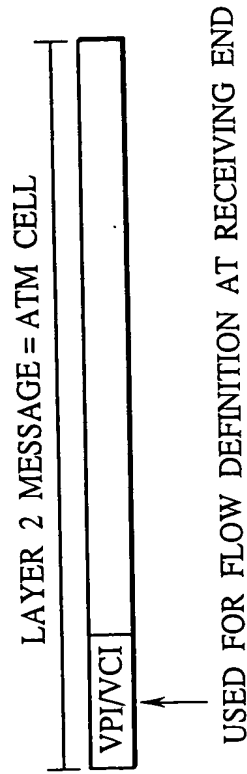


FIG.19

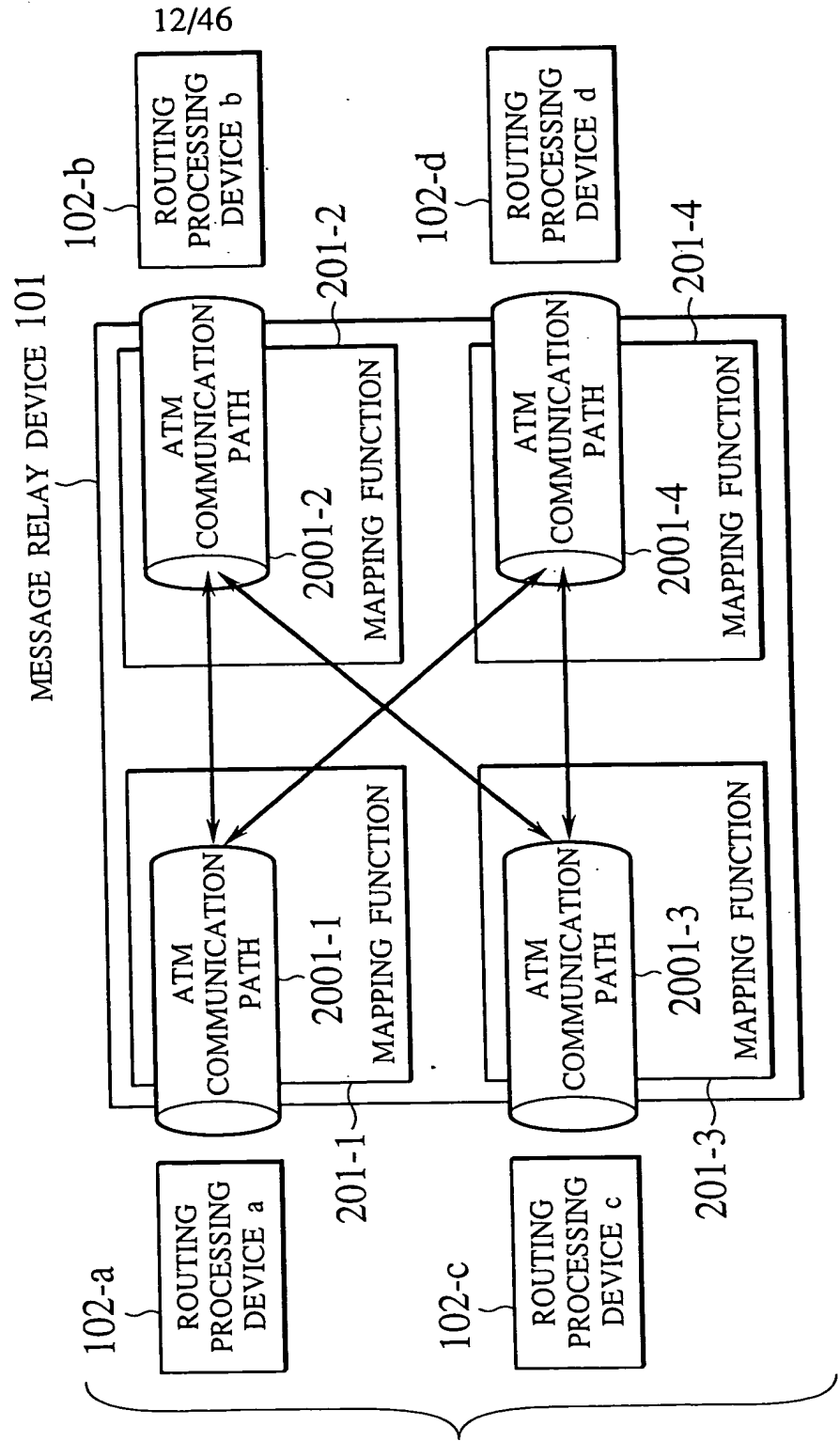
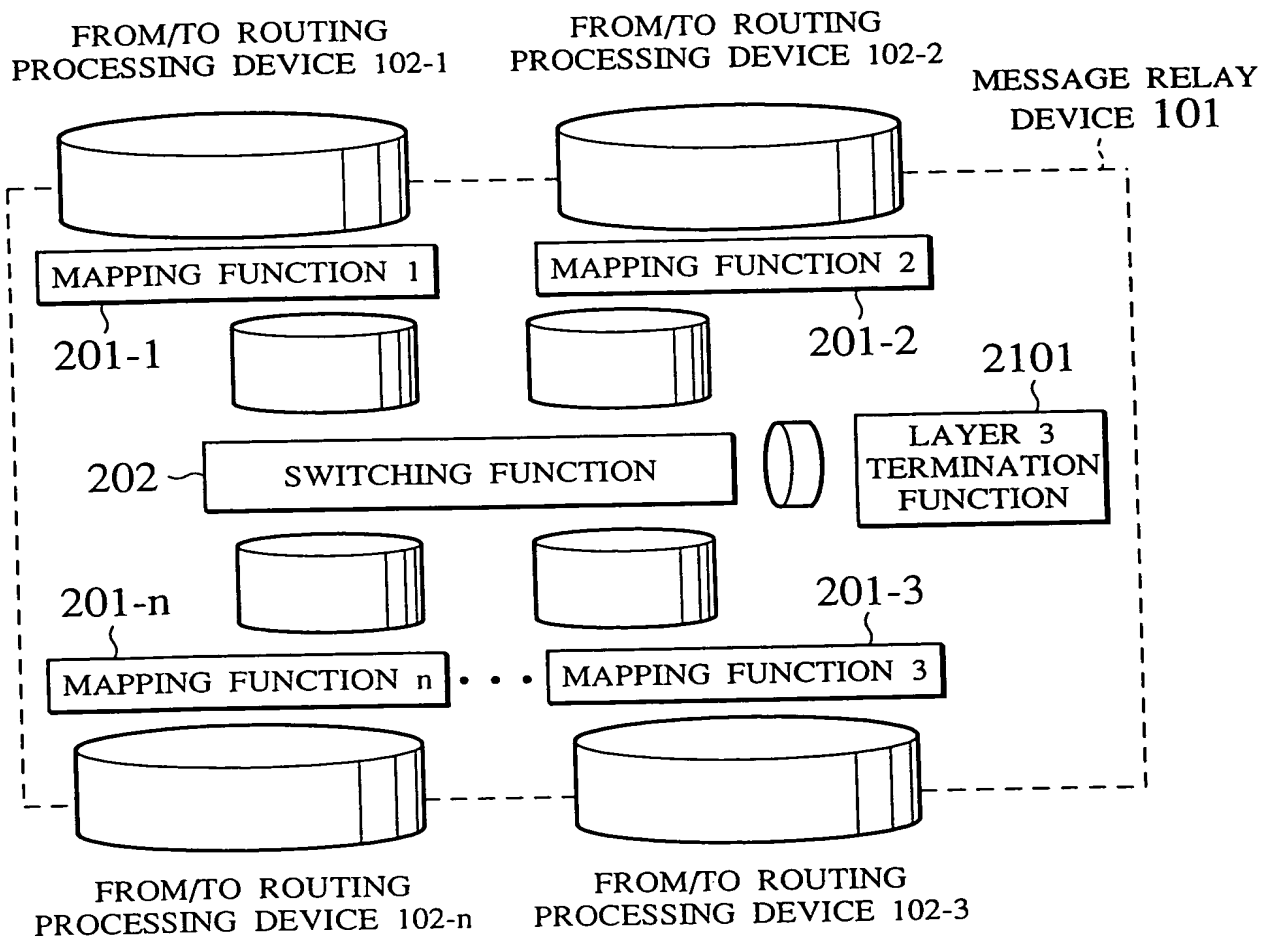


FIG.20

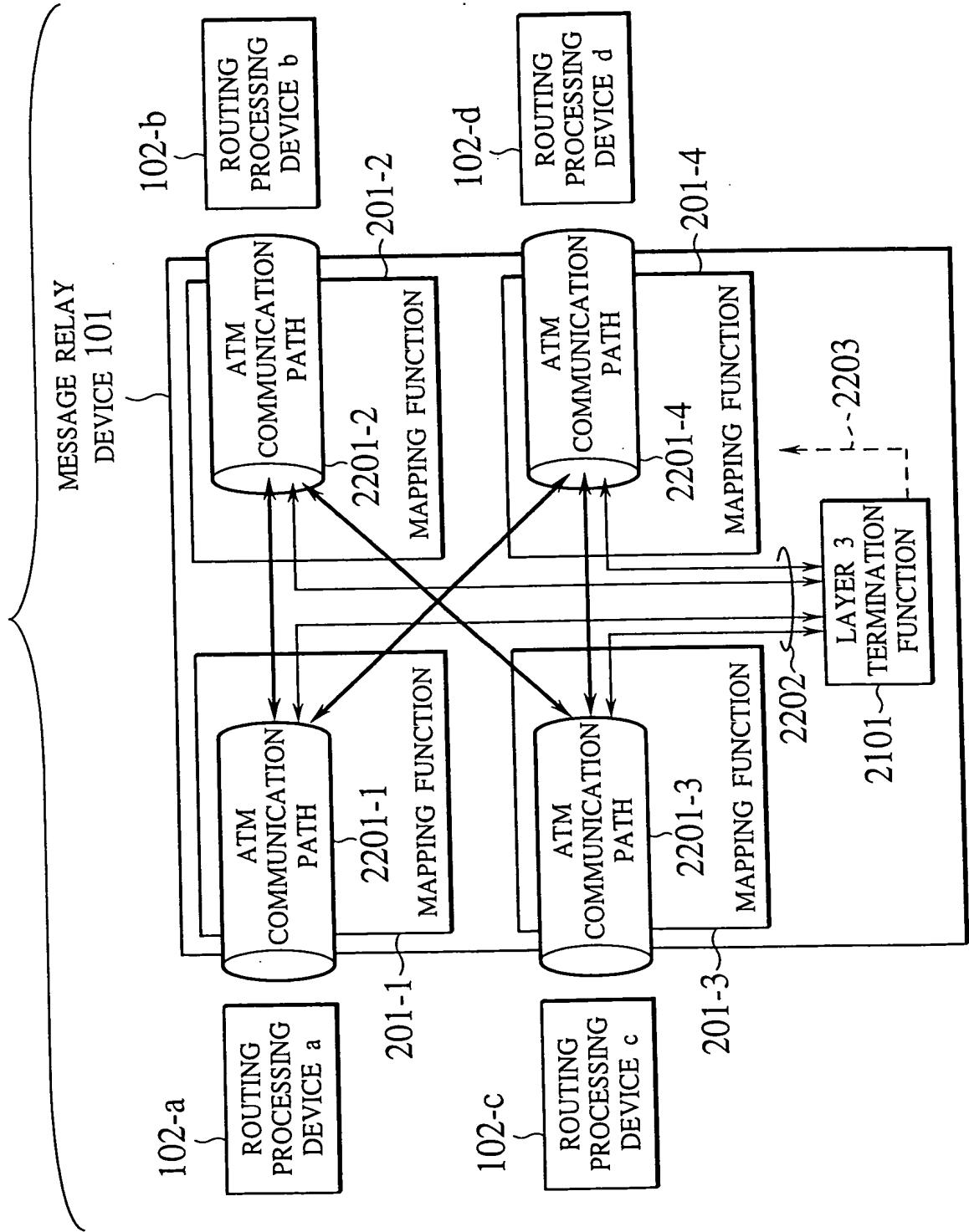
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FIG.21



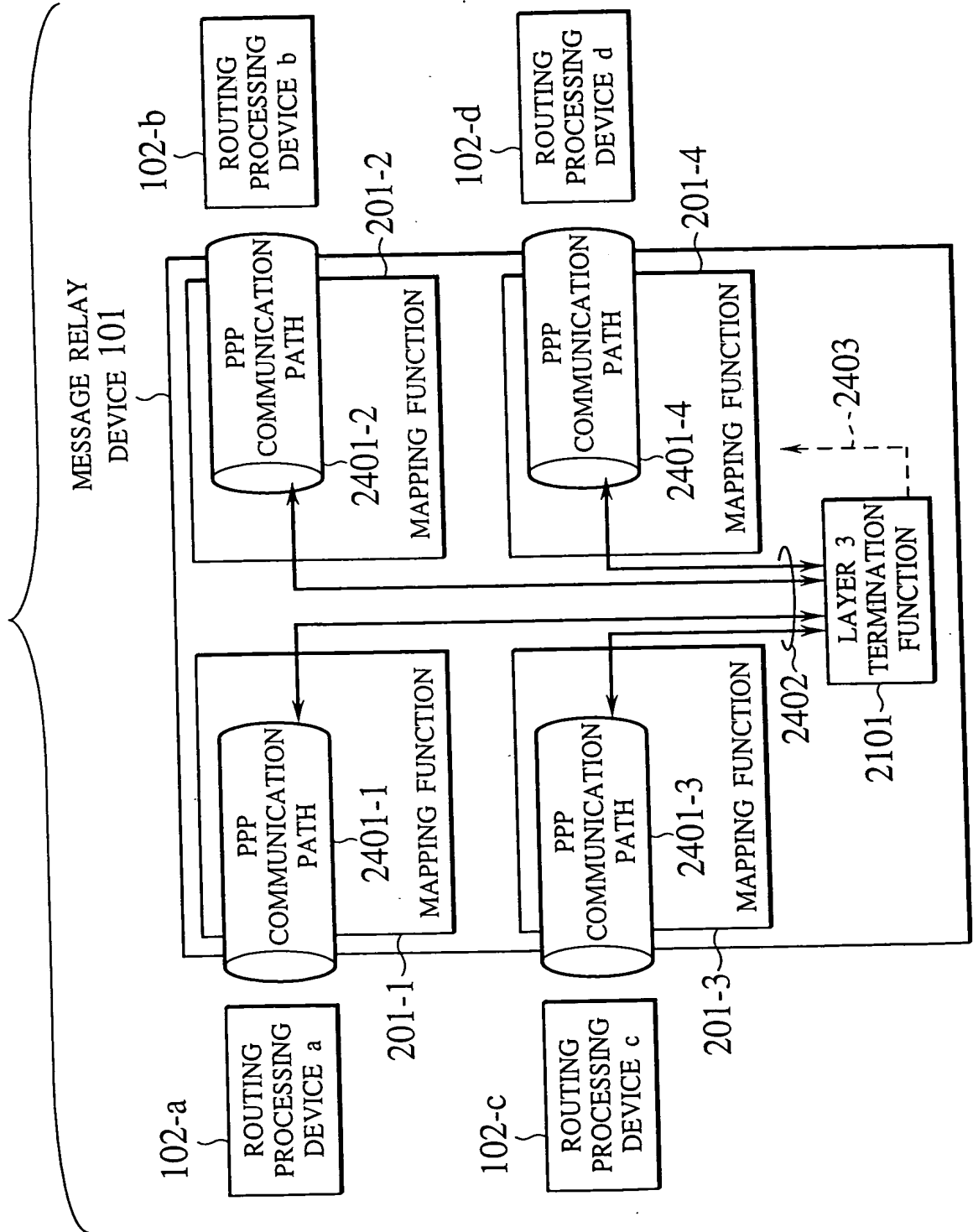
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FIG.22



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FIG. 23



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FIG.24

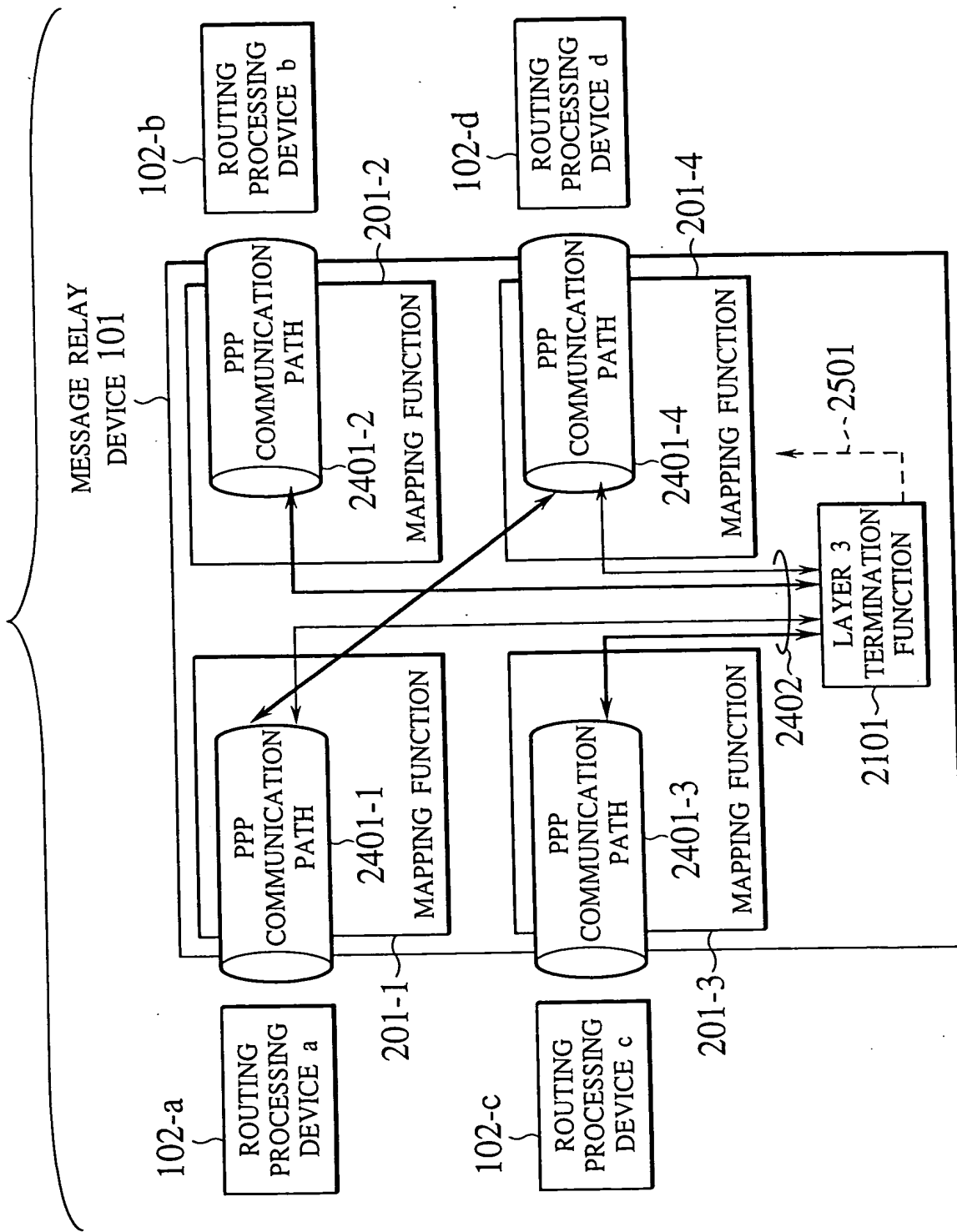
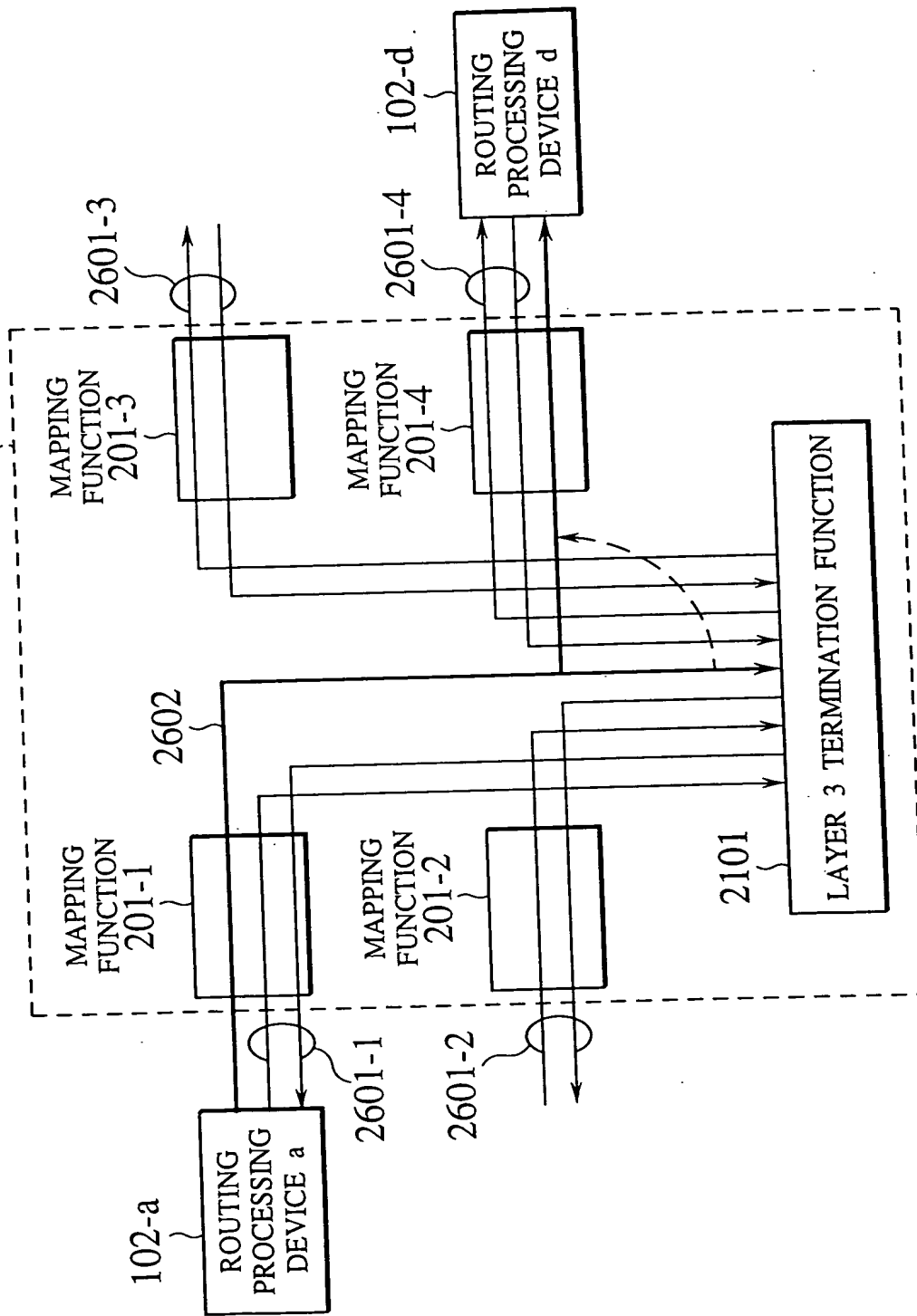
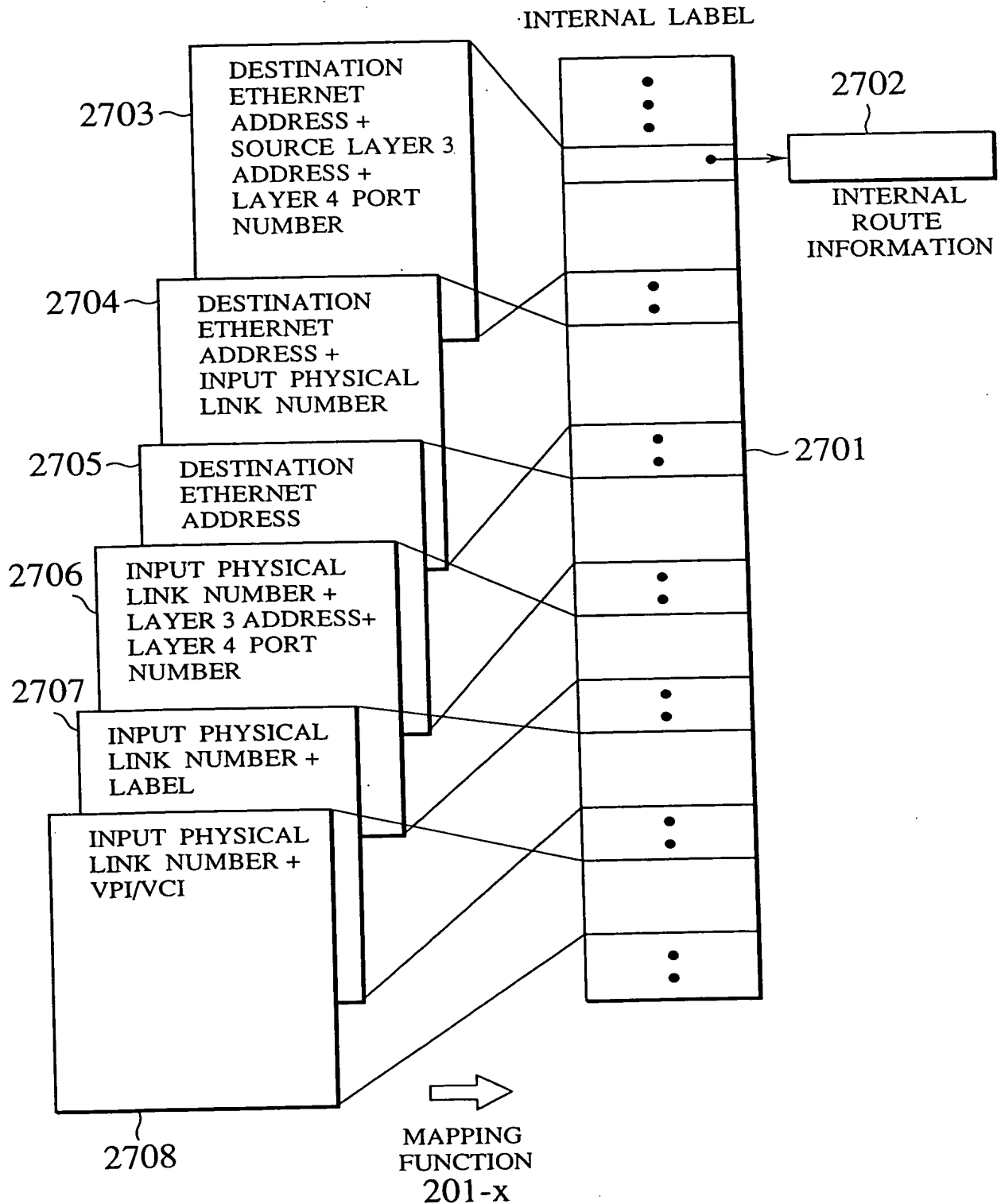


FIG.25
 MESSAGE RELAY DEVICE 101



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FIG.26



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FIG.27

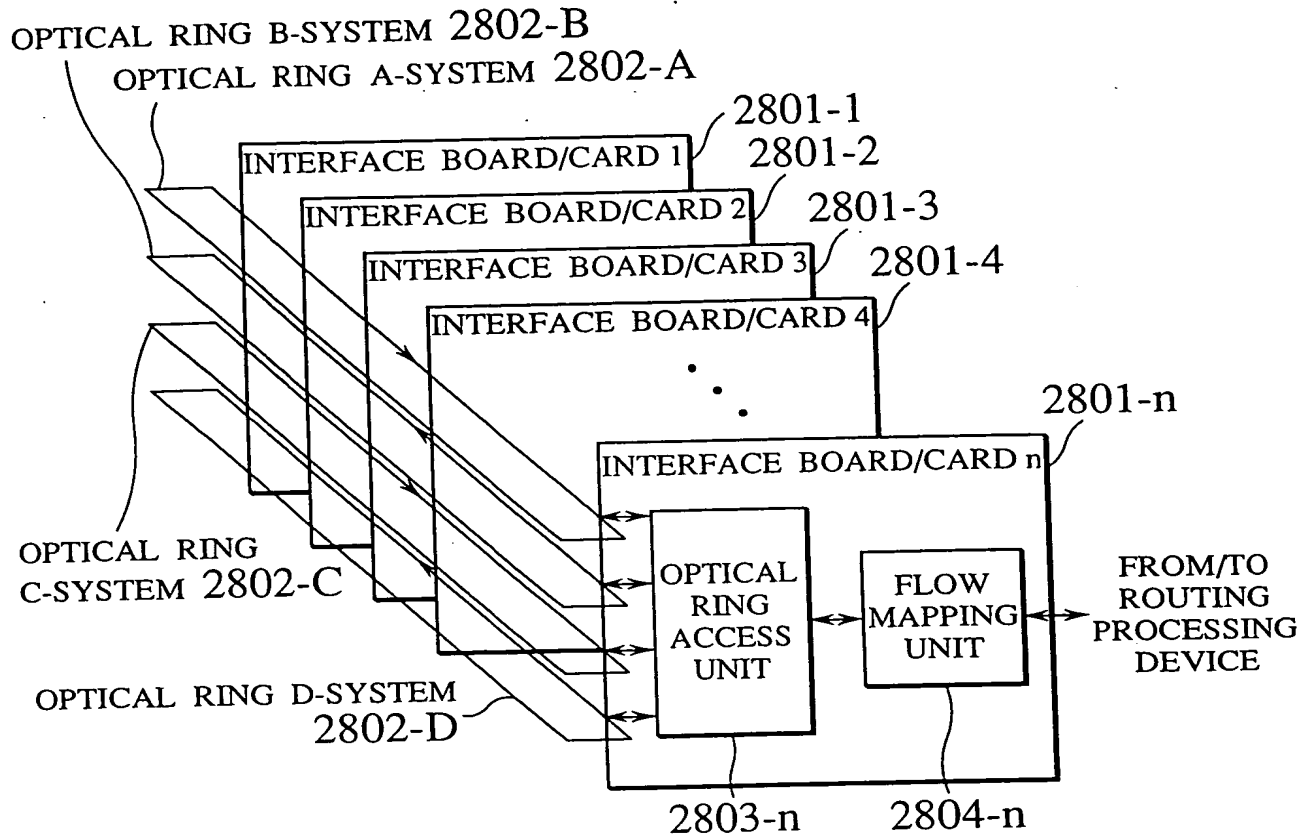
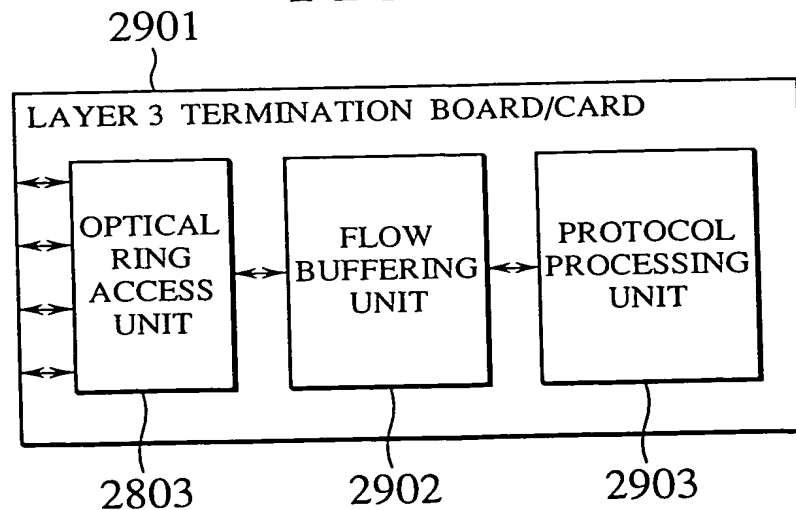


FIG.28



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FIG.29

MESSAGE RELAY DEVICE 101

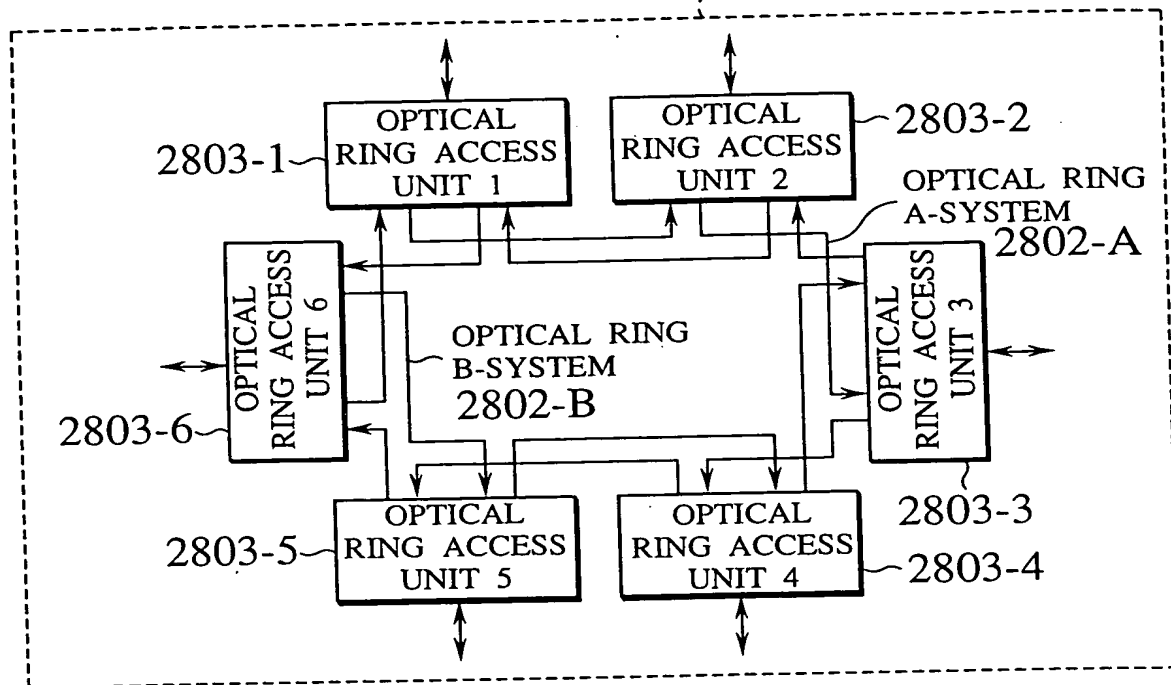
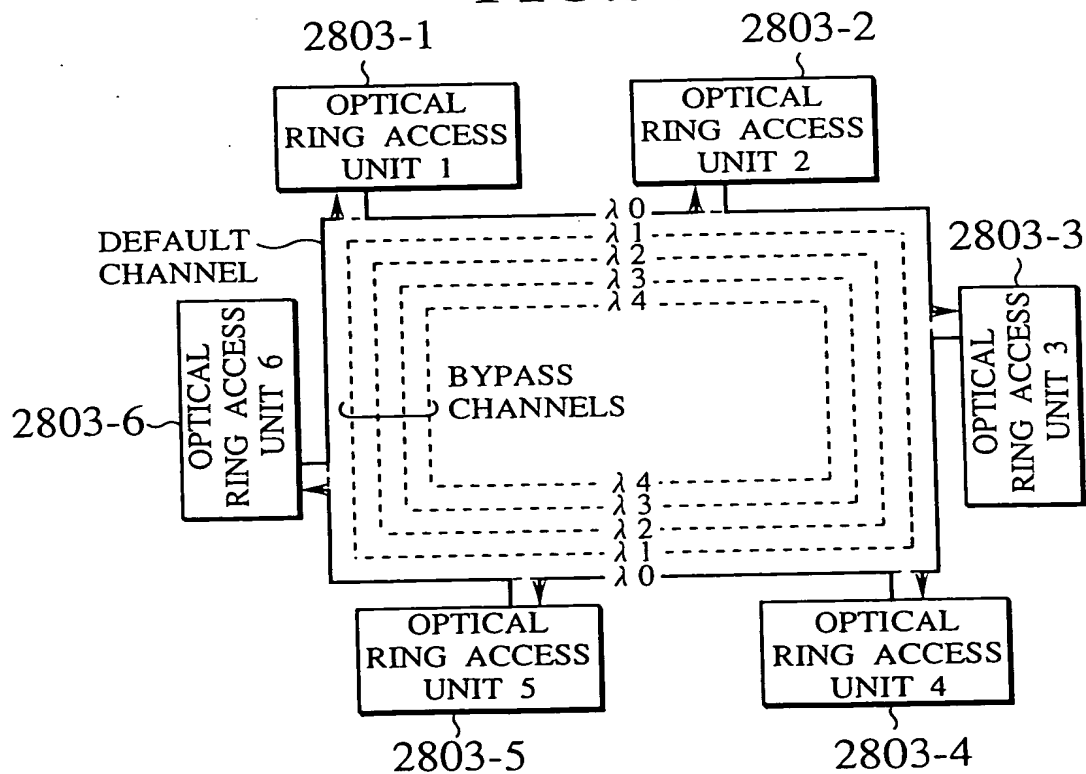


FIG.30



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FIG.31

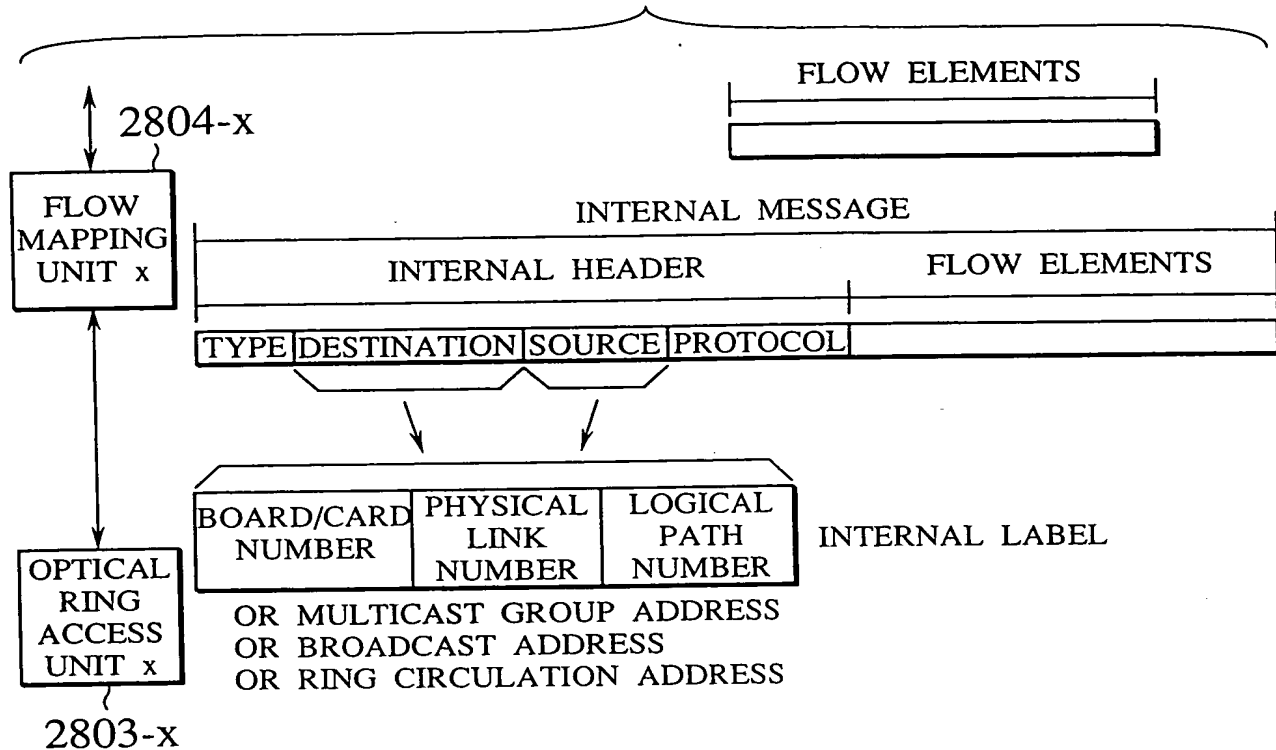
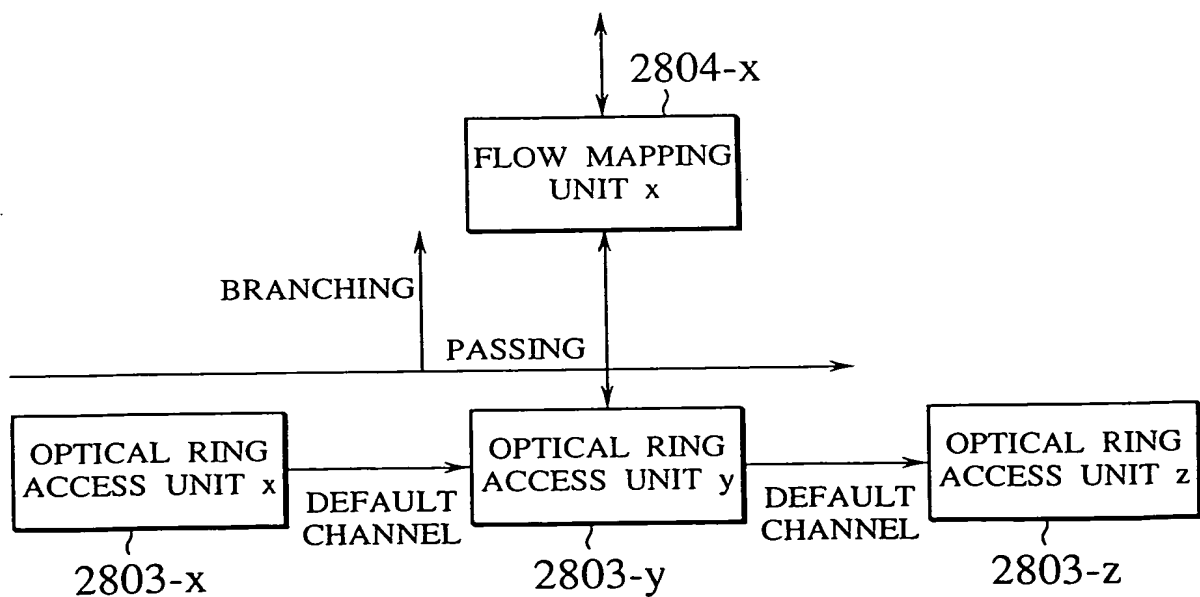
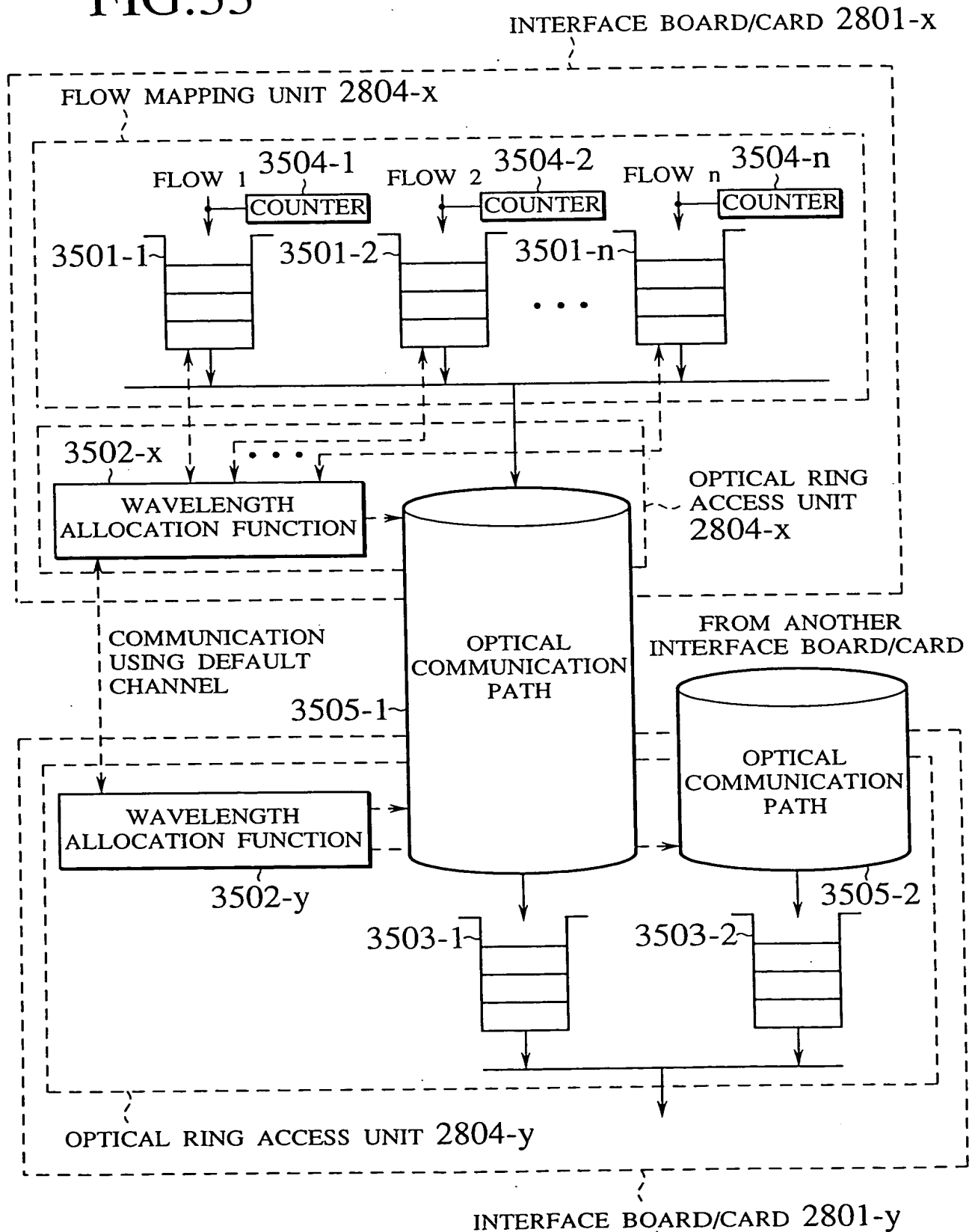


FIG.32



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FIG.33



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FIG.34A

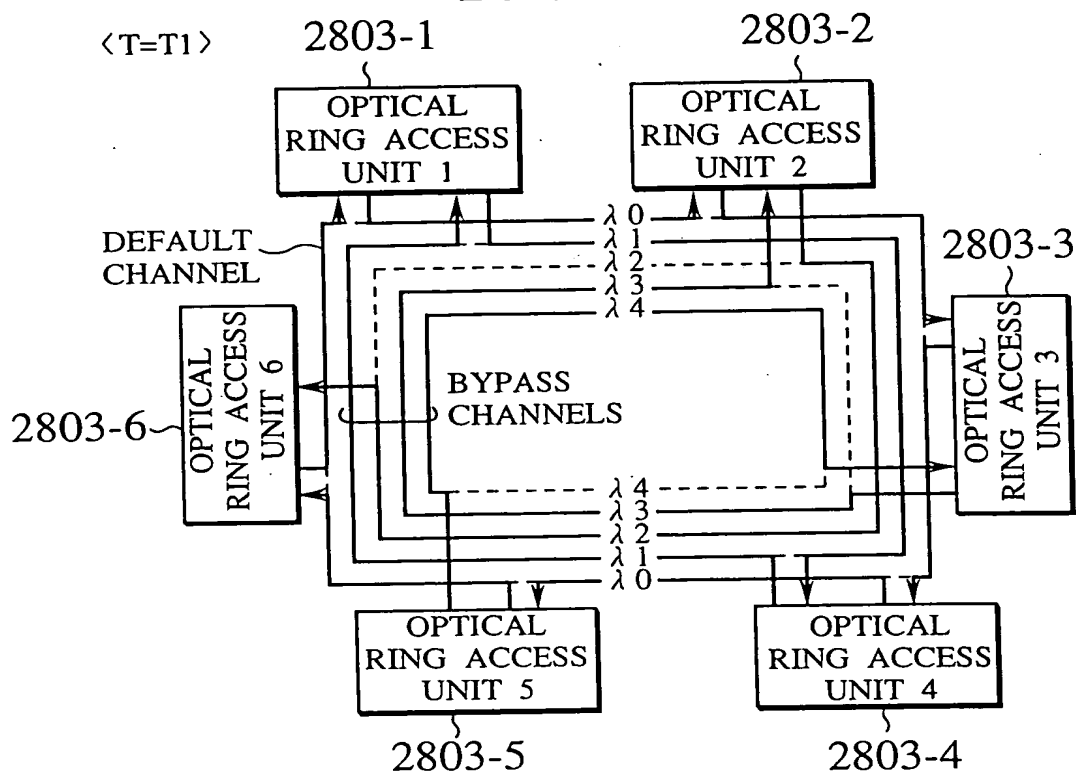


FIG.34B

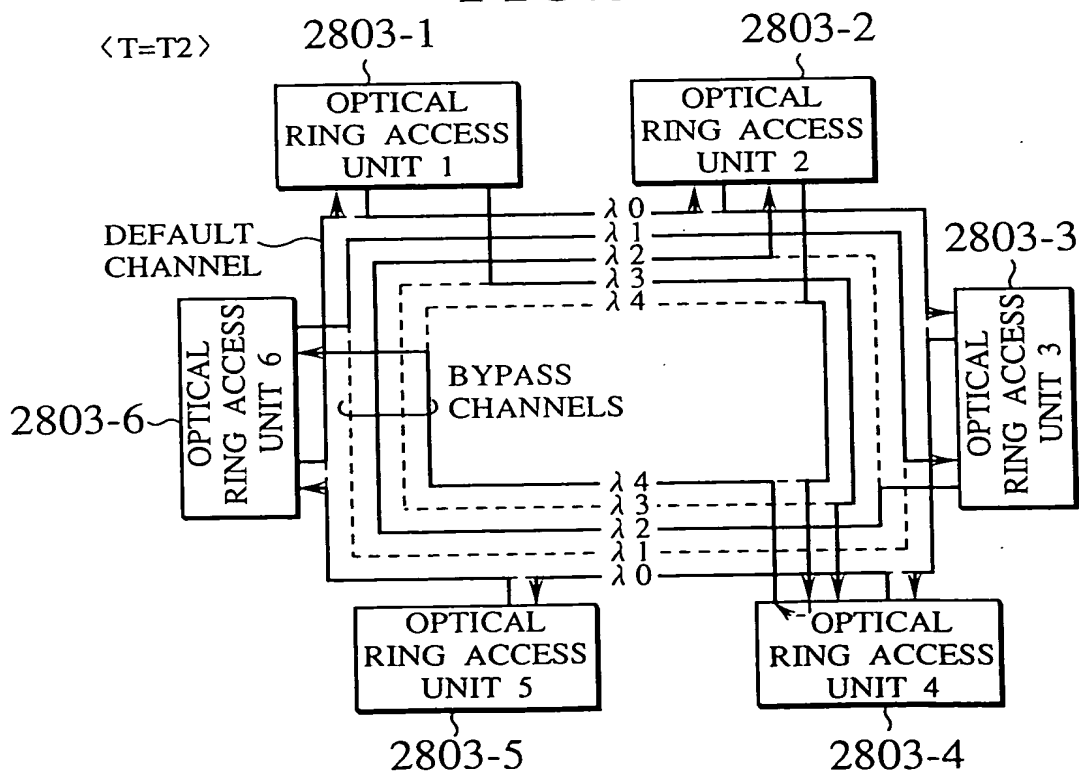
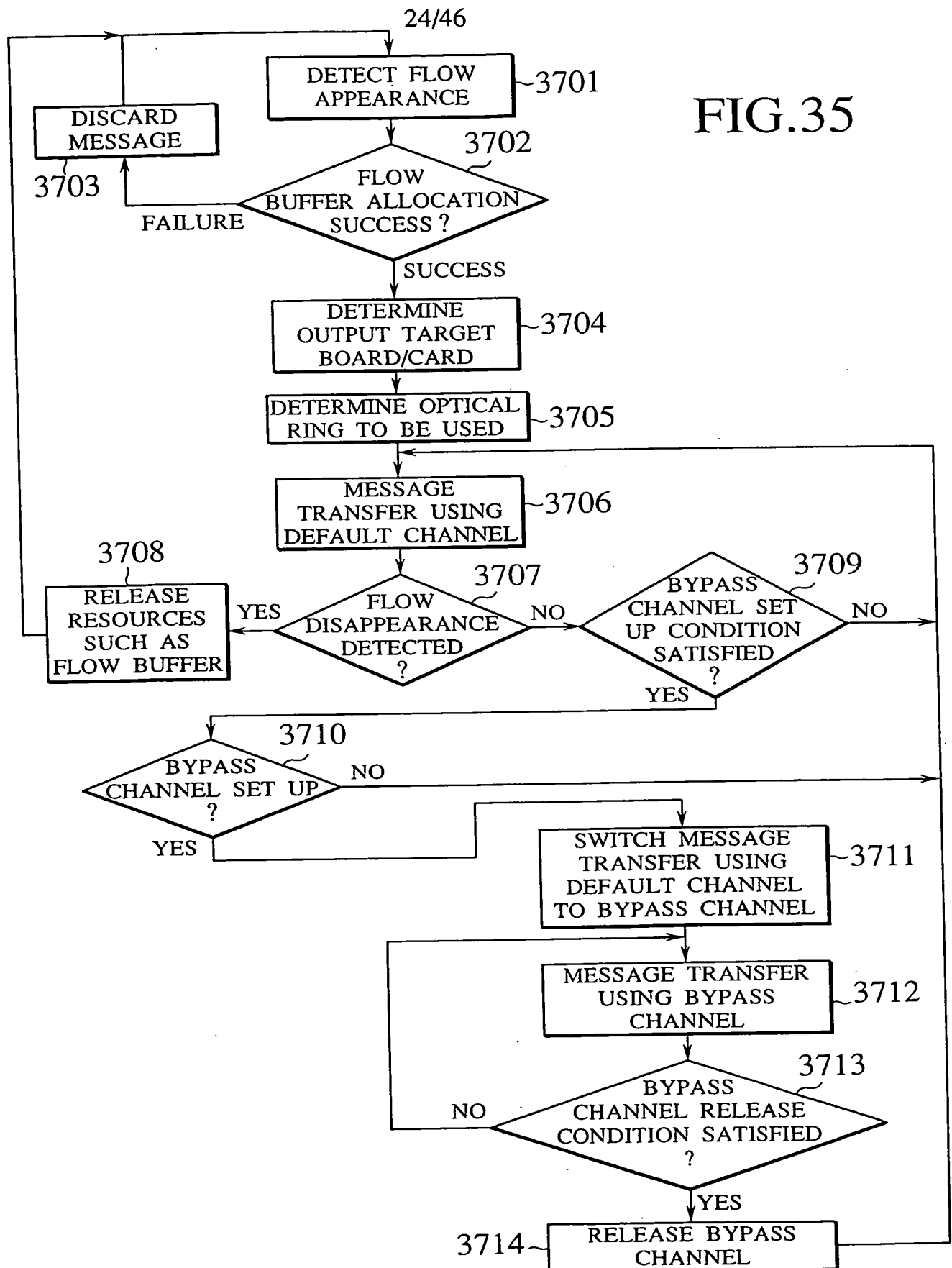
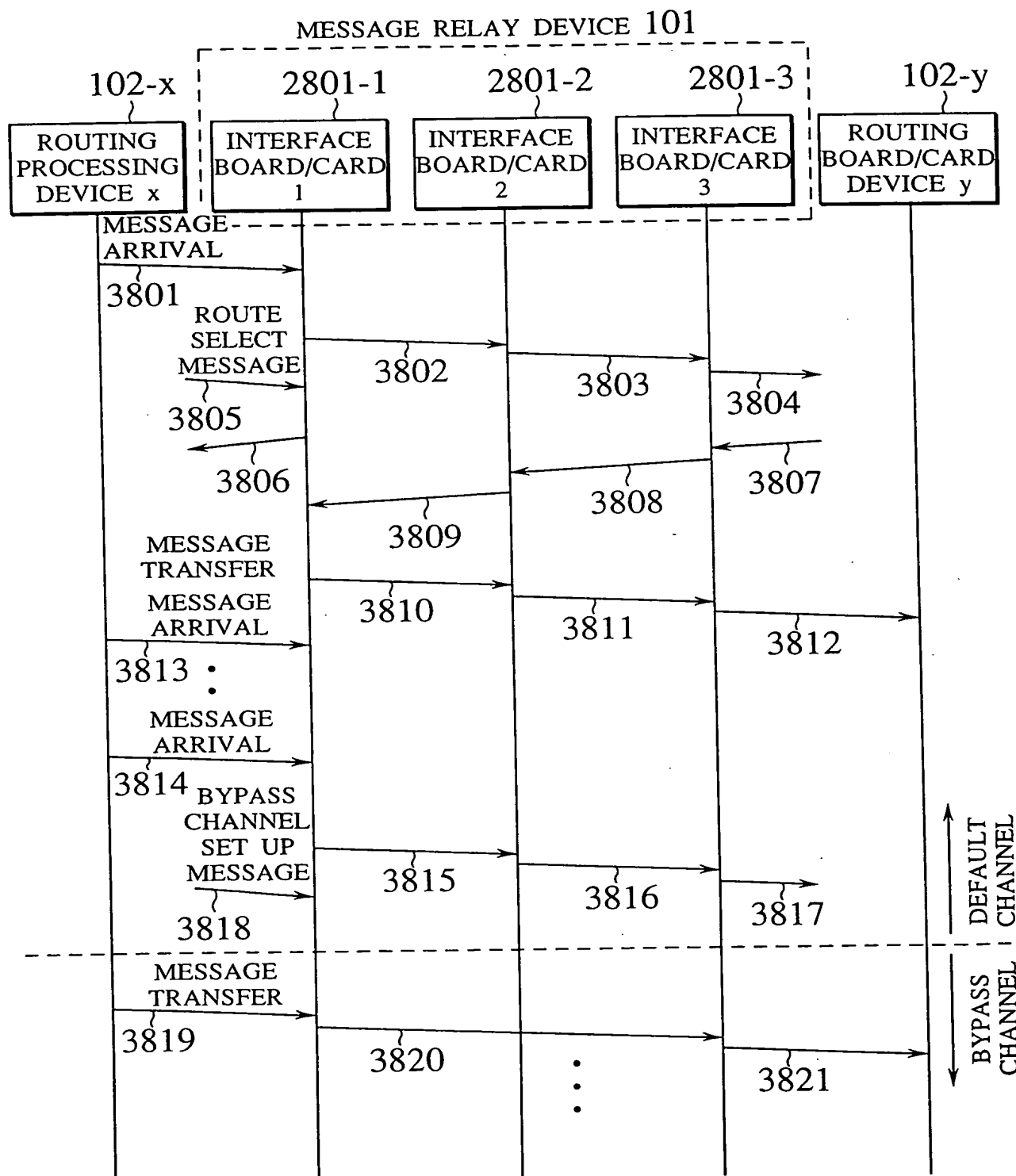


FIG.35

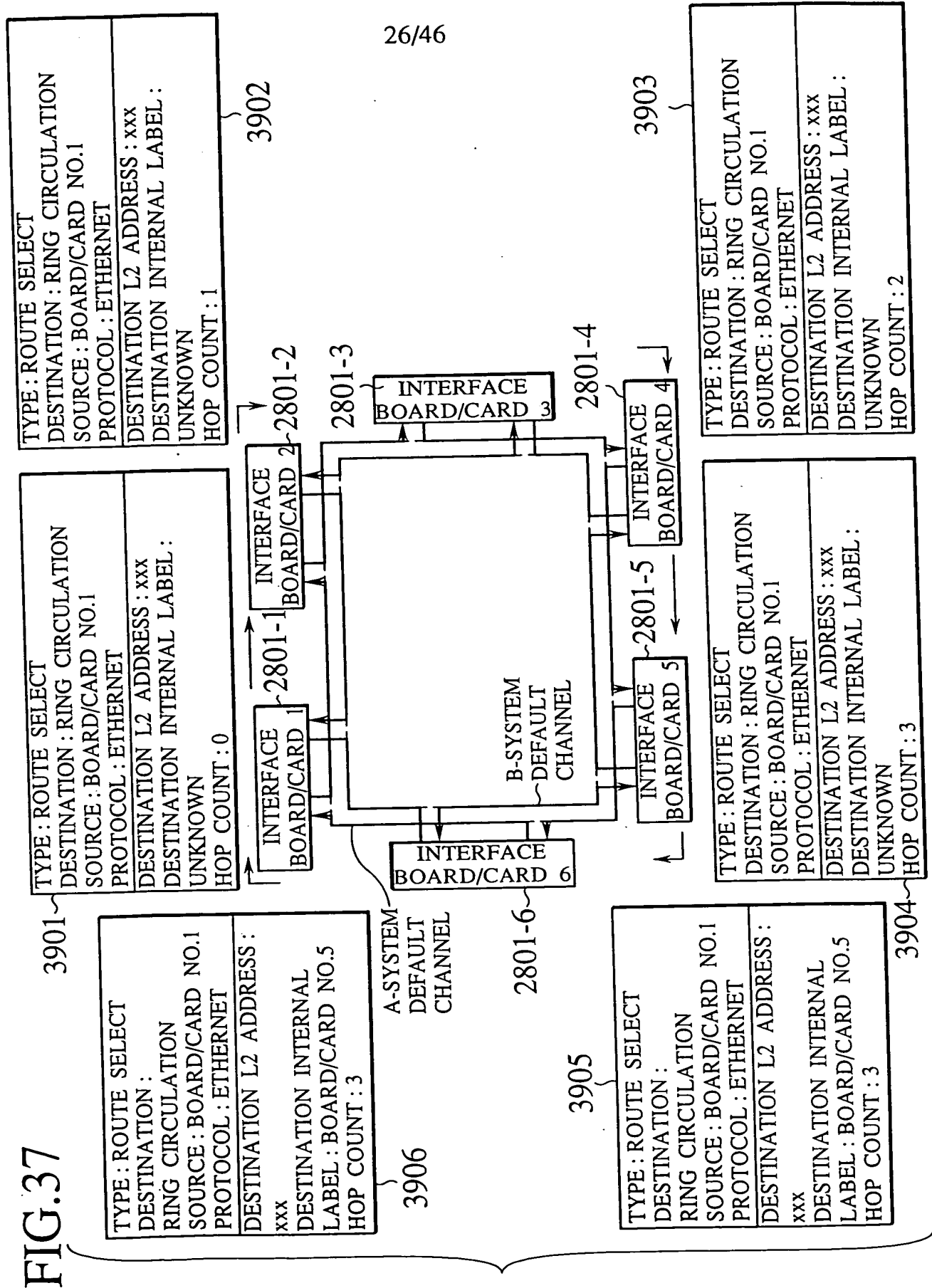


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FIG.36



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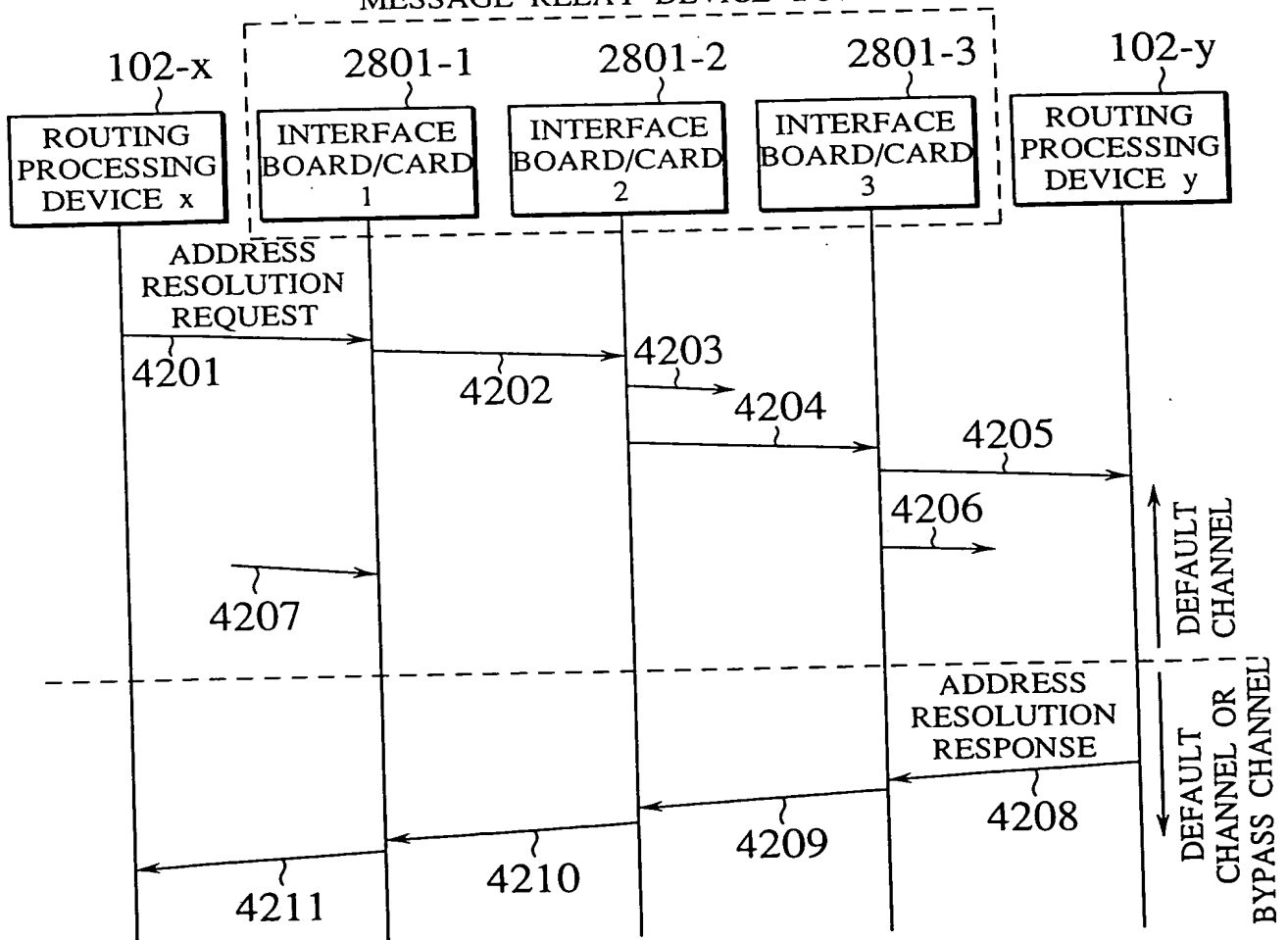
FIG.38

INTERNAL ROUTE INFORMATION HOLDING TABLE 4101

INTERNAL LABEL →			
	CLOCKWISE	A-SYSTEM	BYPASS CHANNEL
	NOT SEARCHED	-----	-----
	PRIMARY ROUTE	SELECTED ROUTE	SELECTED CHANNEL

FIG.39

MESSAGE RELAY DEVICE 101



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FIG.40

WAVELENGTH ALLOCATION TABLE

$\lambda 0$	DEFAULT CHANNEL	
$\lambda 1$	BOARD/CARD 1	BOARD/CARD 4
$\lambda 2$	BOARD/CARD 3	BOARD/CARD 2
⋮		
$\lambda 1$	UNUSED	-----
⋮		
λn	BOARD/CARD 2	BOARD/CARD 3

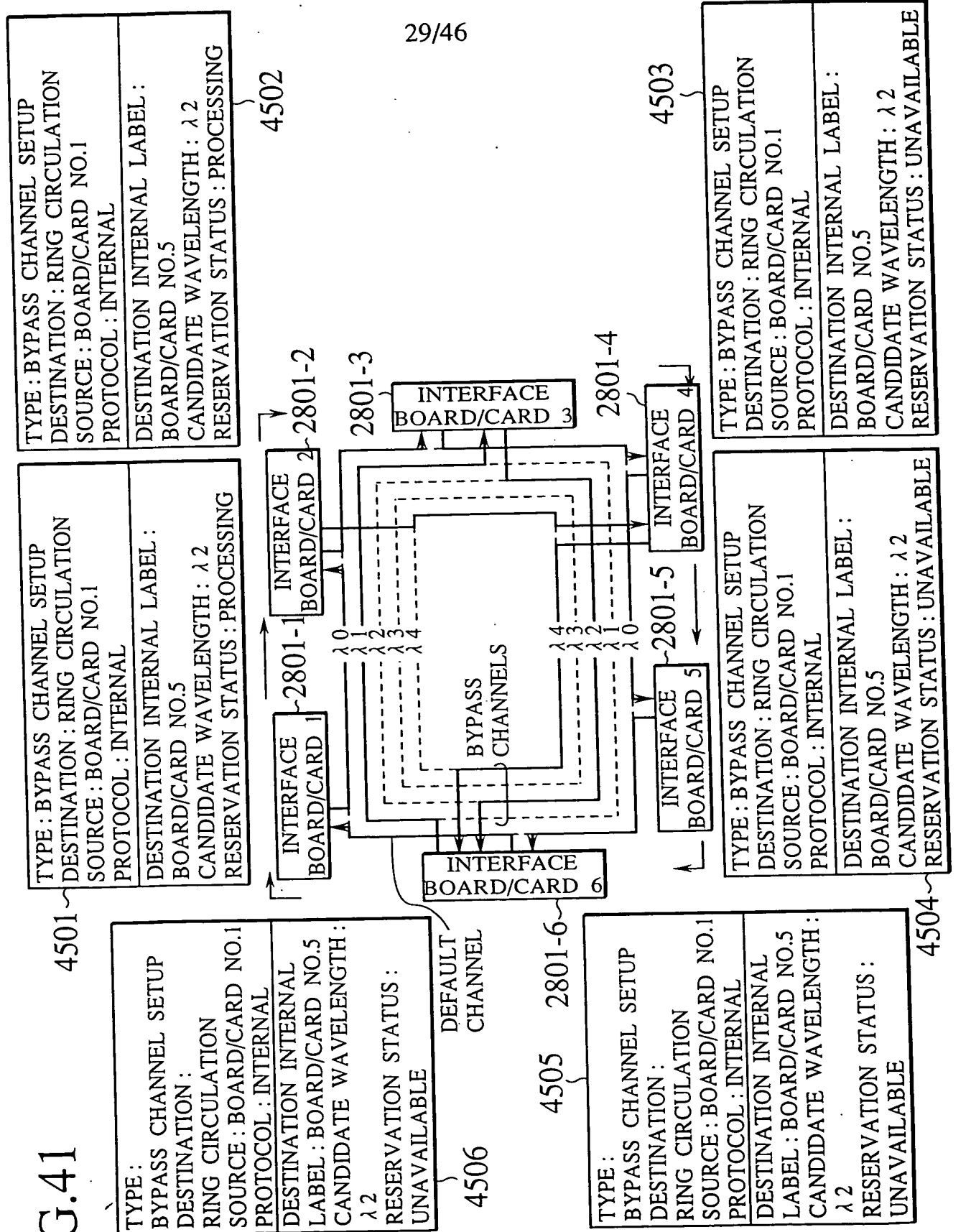
WAVELENGTH

TRANSMITTING
END

RECEIVING
END

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FIG.41



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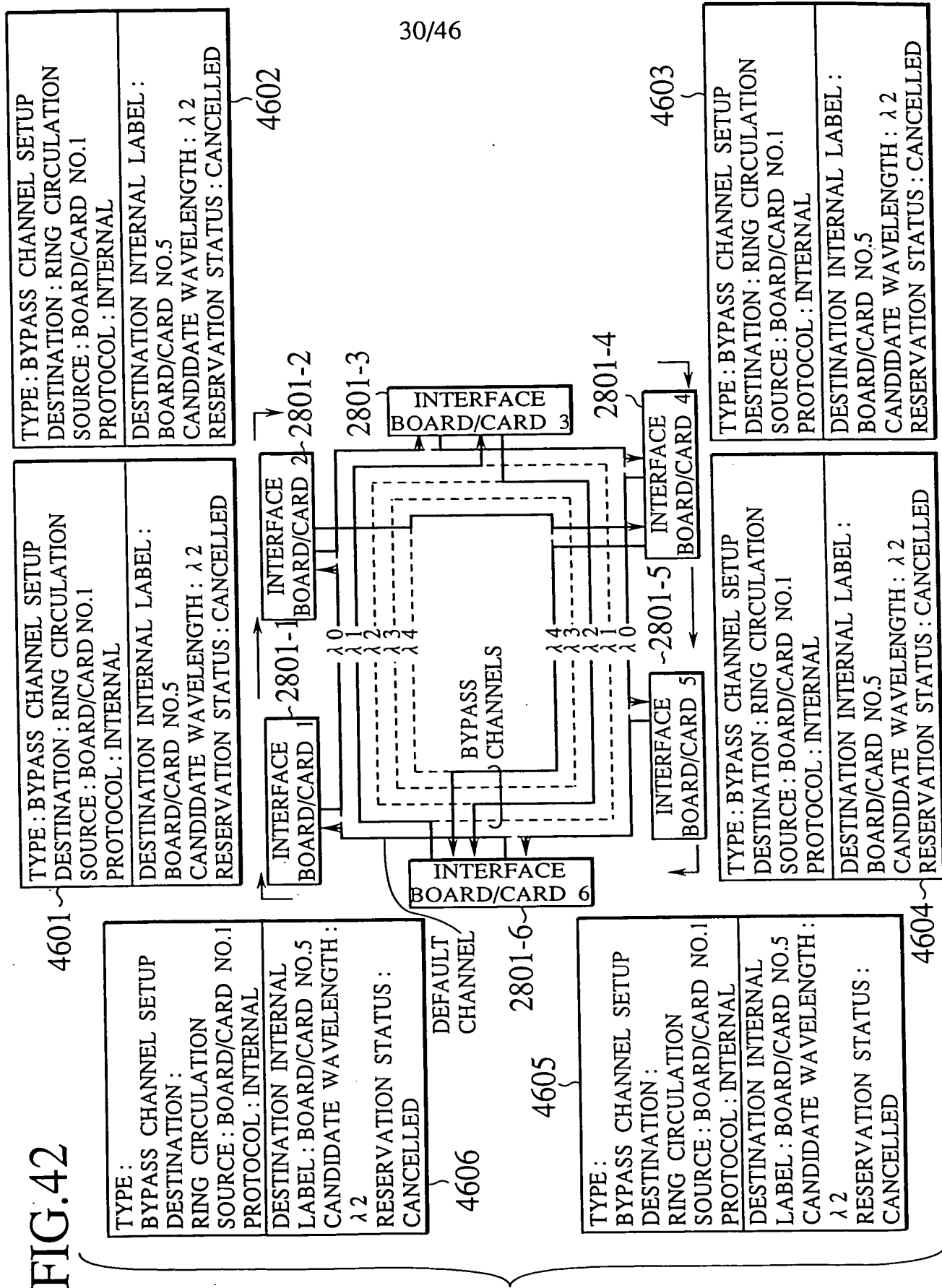
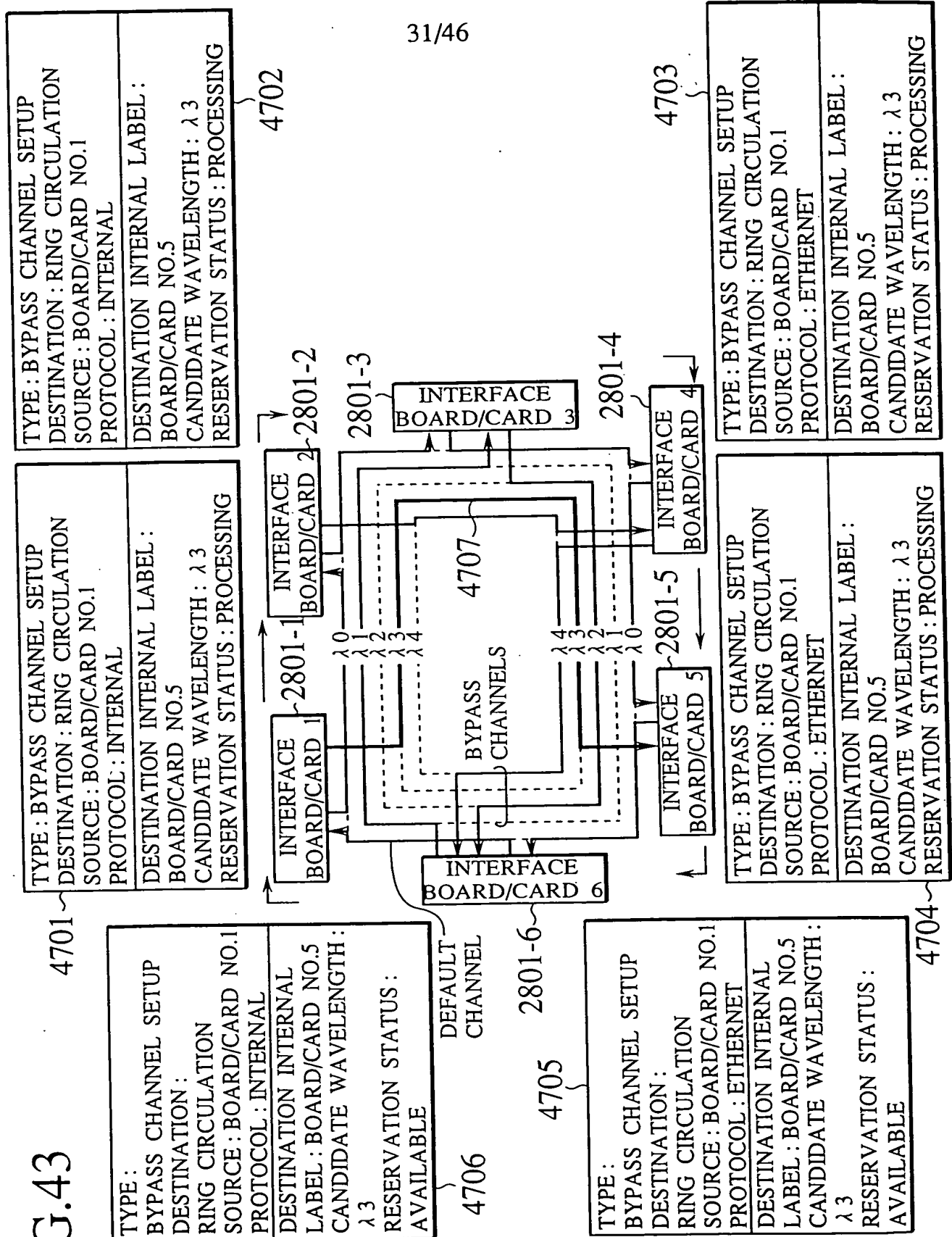
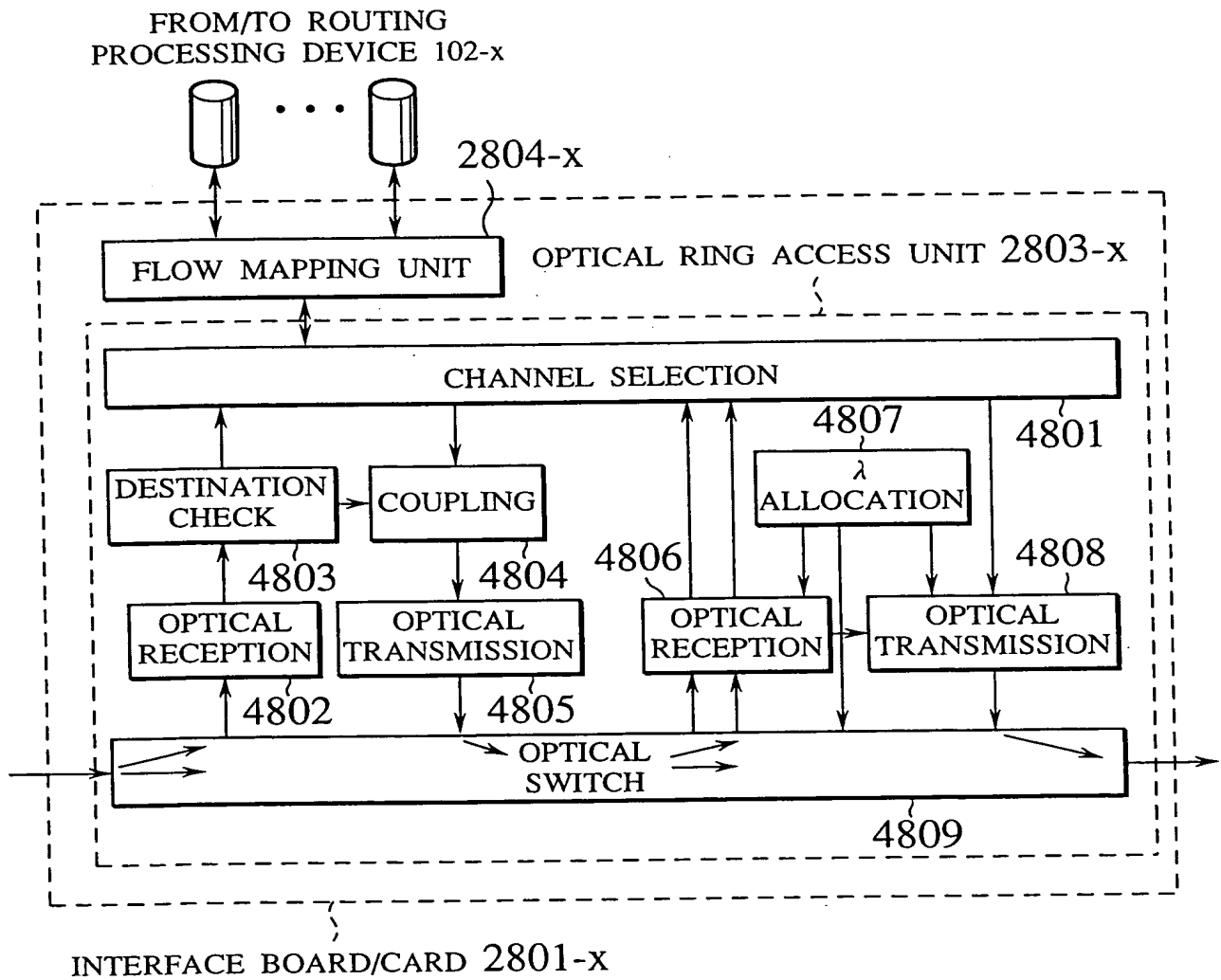


FIG.43



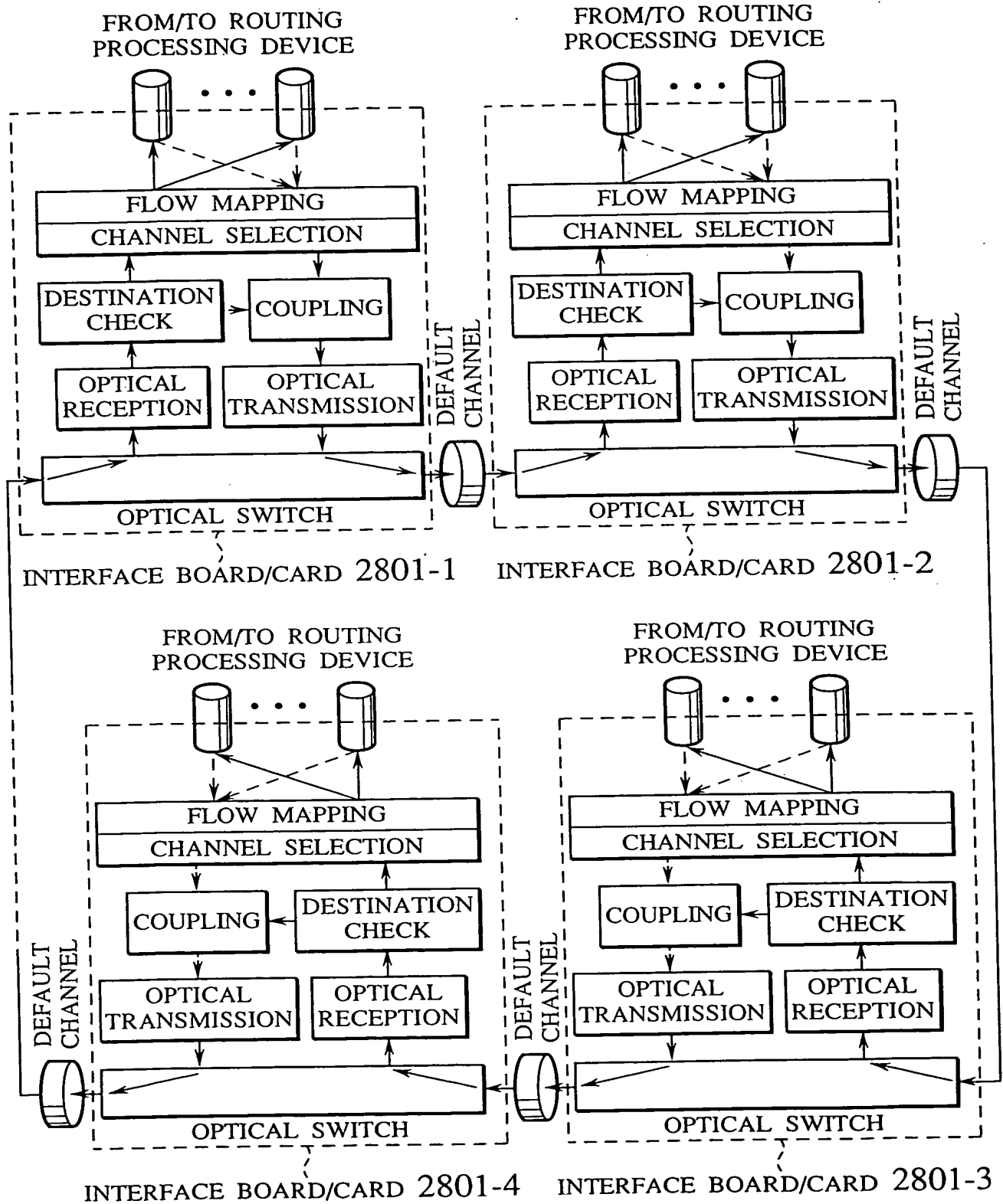
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FIG.44



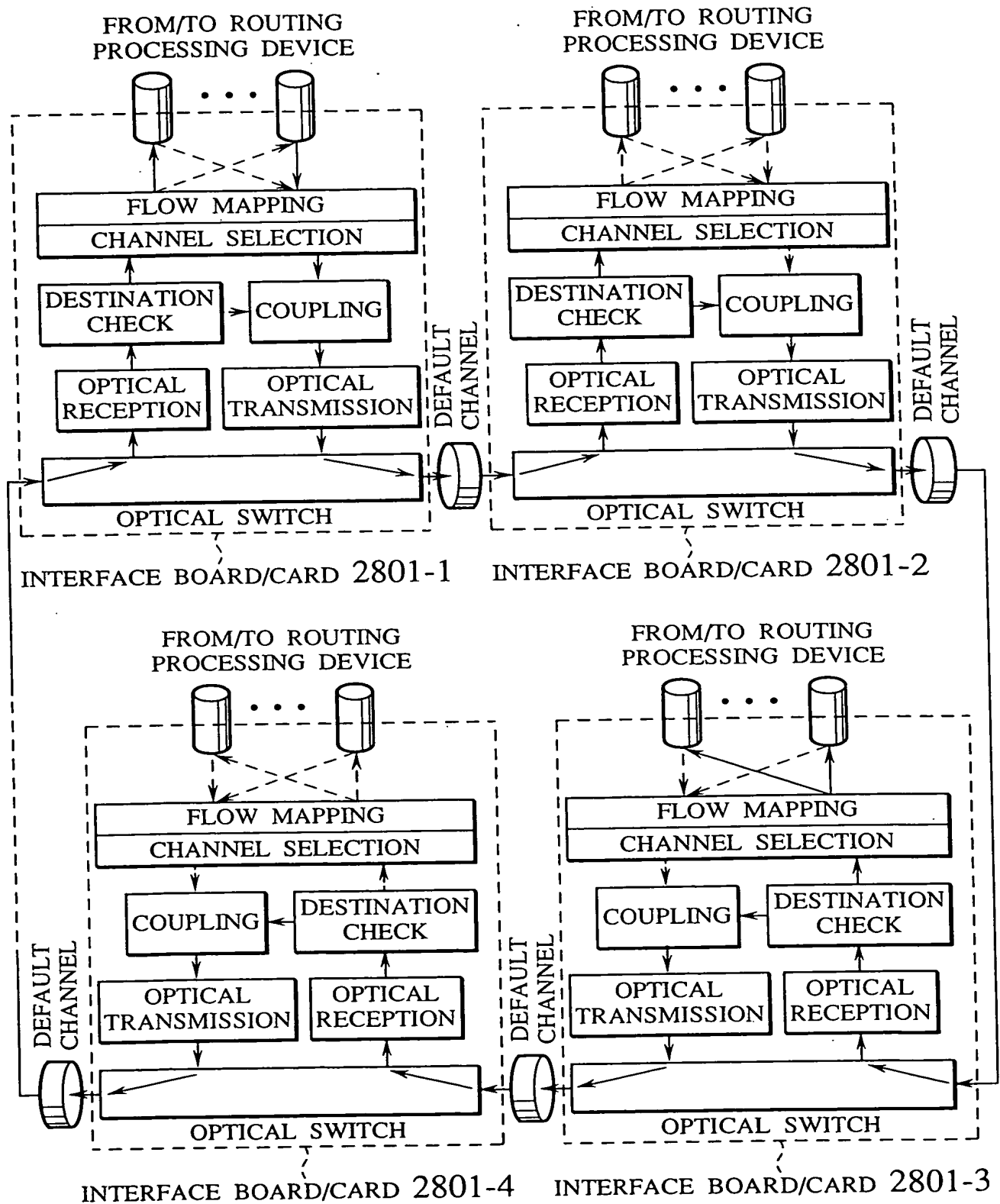
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FIG.45



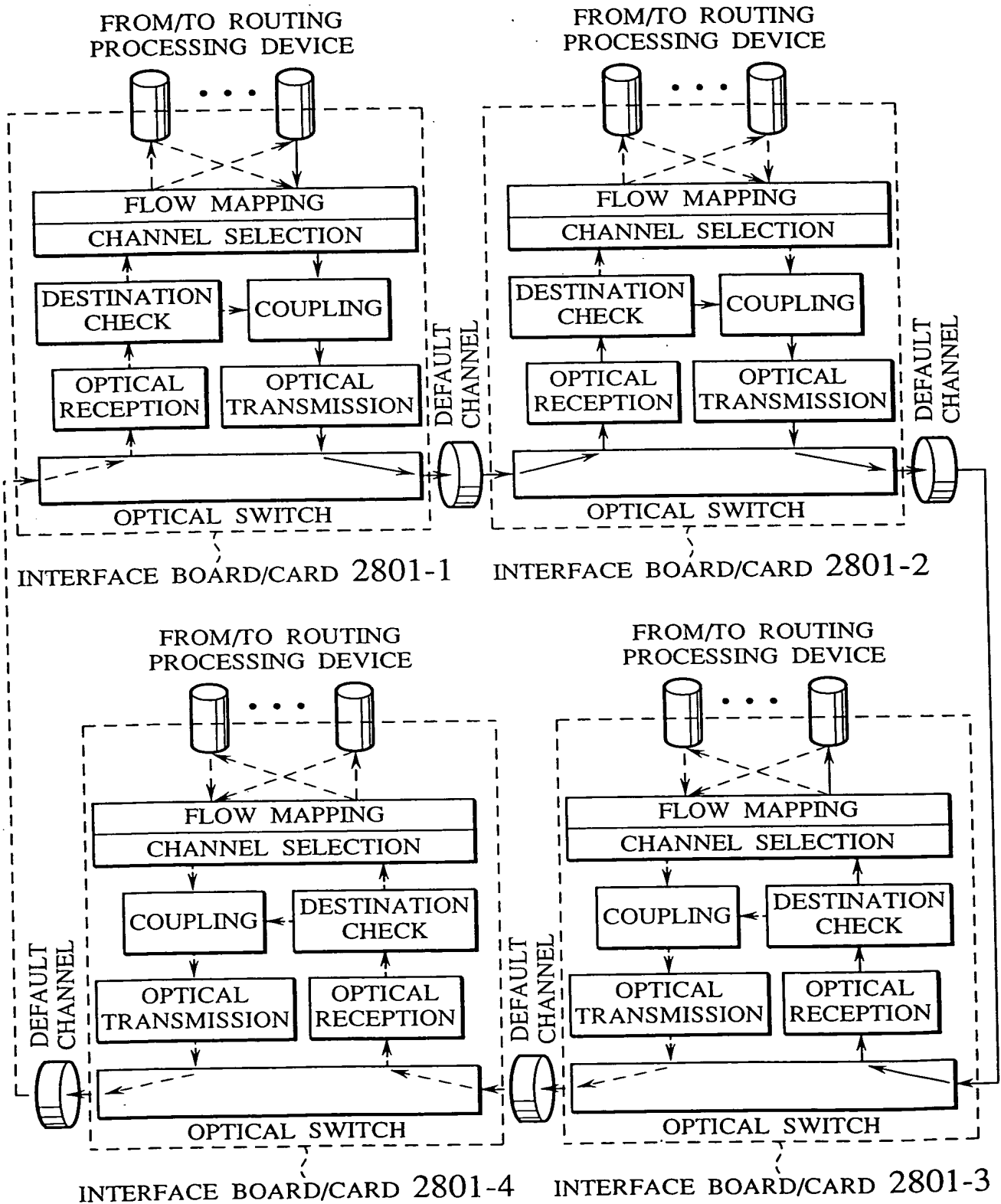
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FIG.46



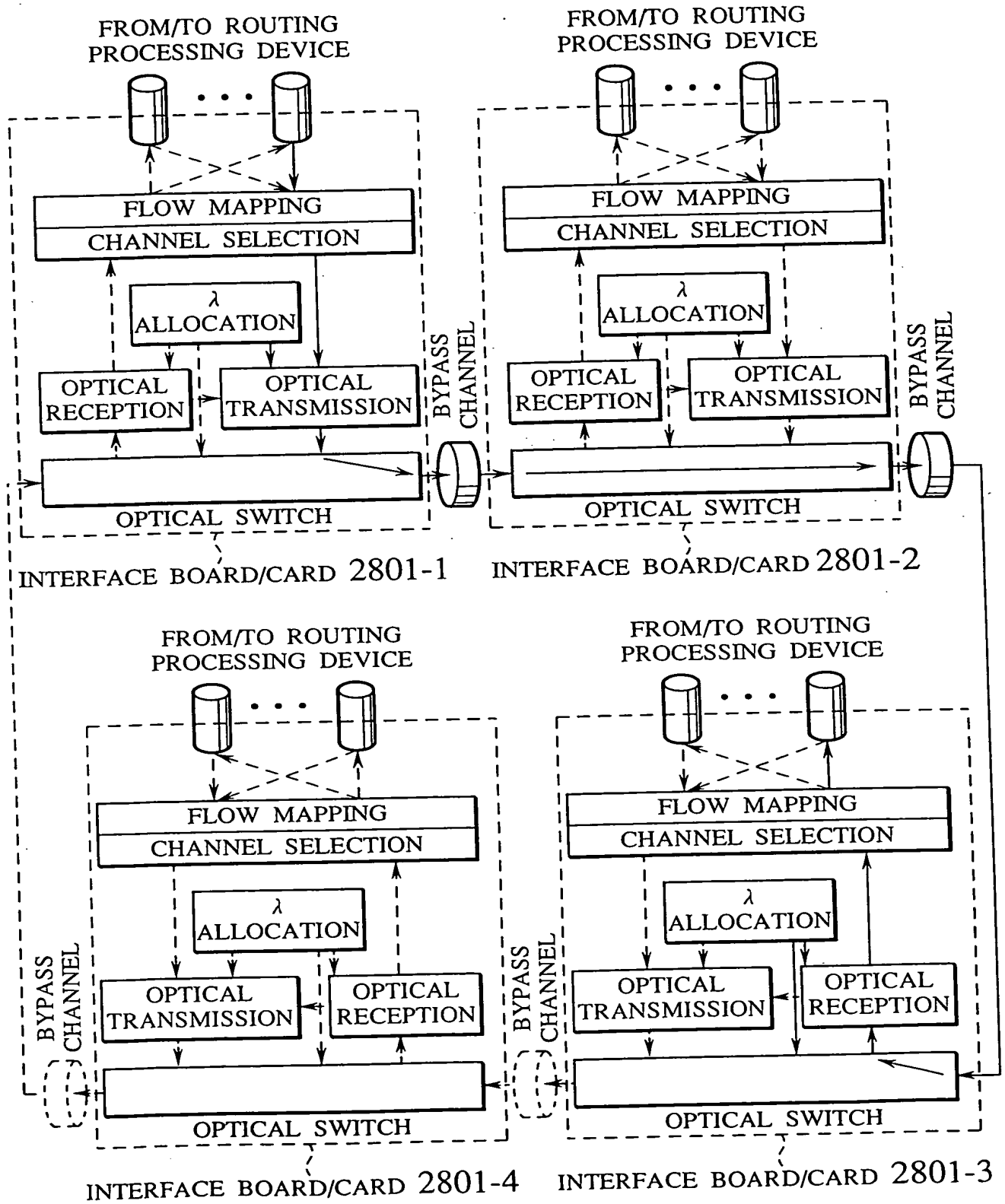
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FIG.47



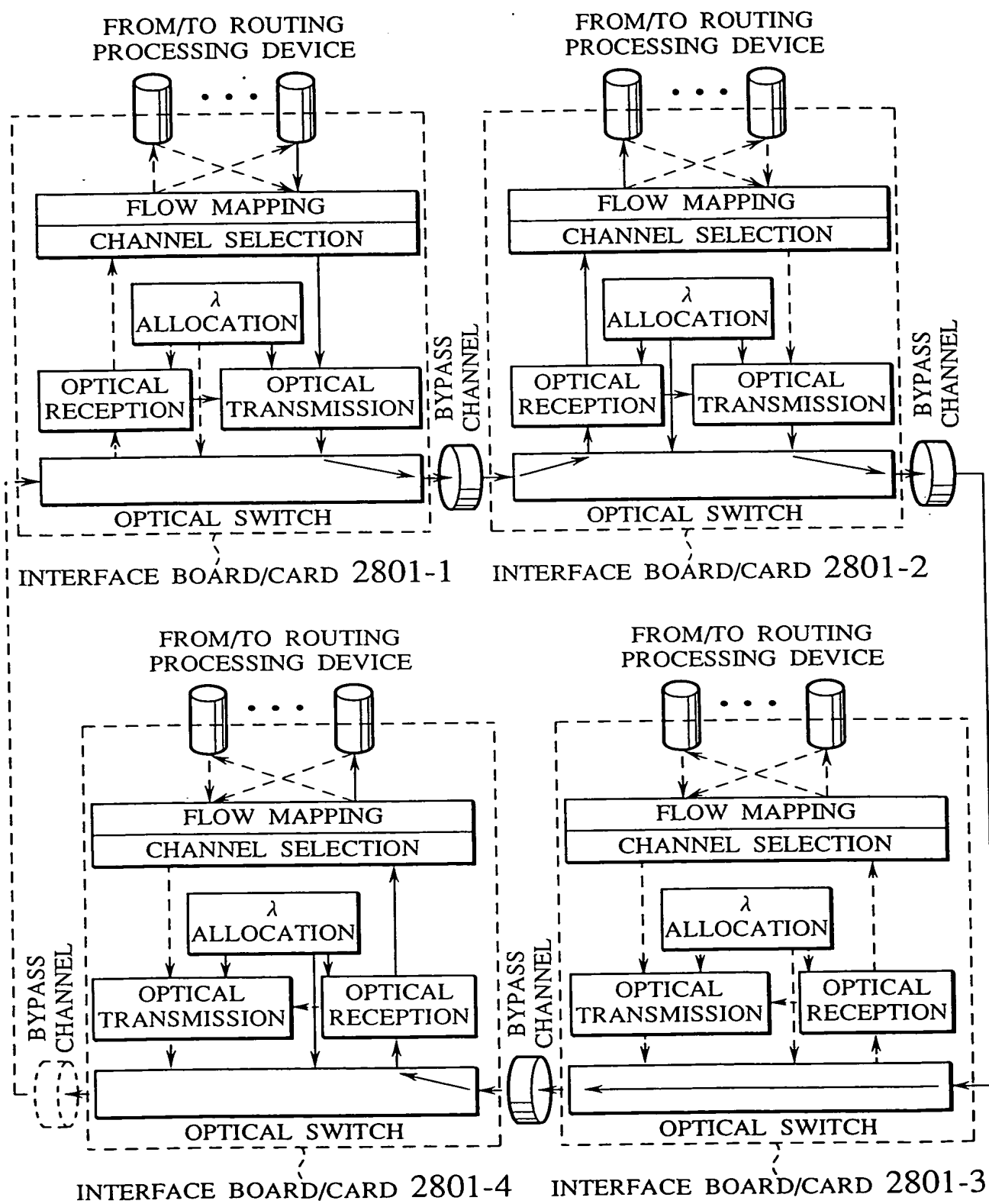
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FIG.48



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FIG.49



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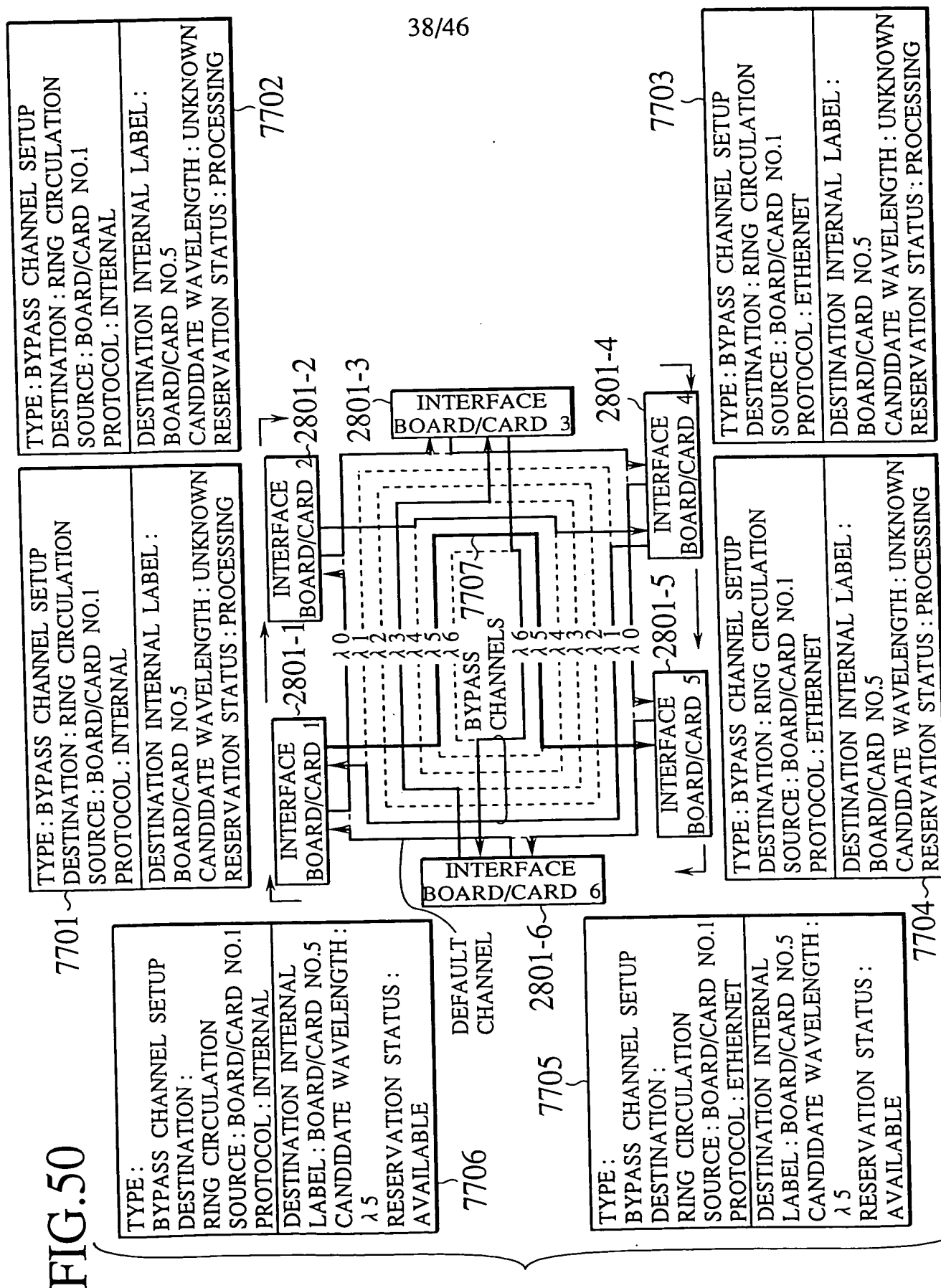
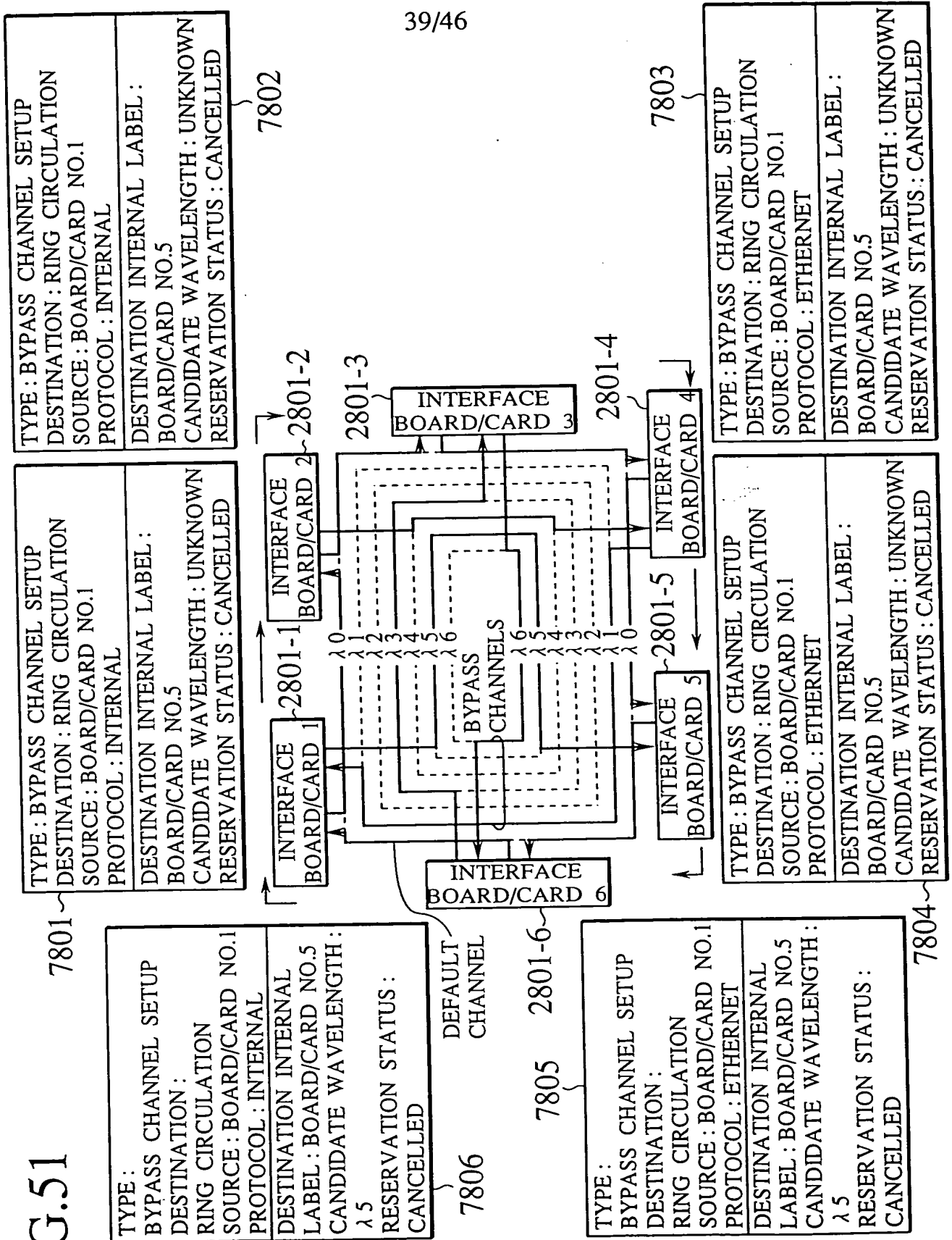
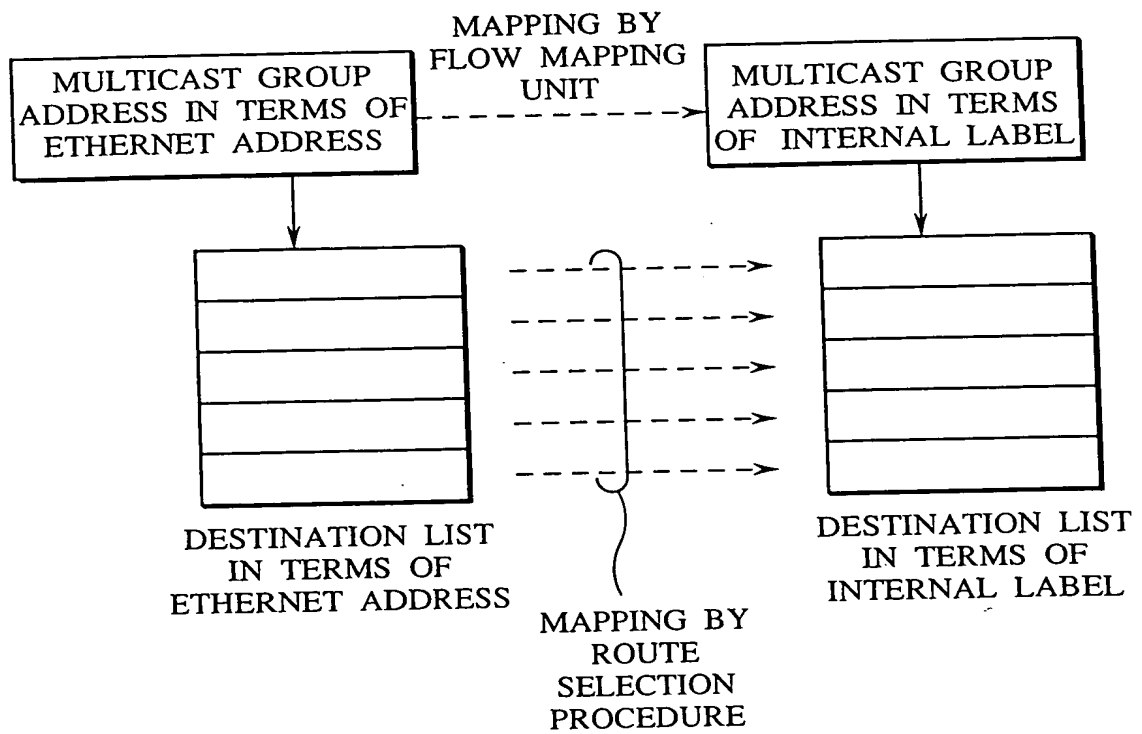


FIG. 51

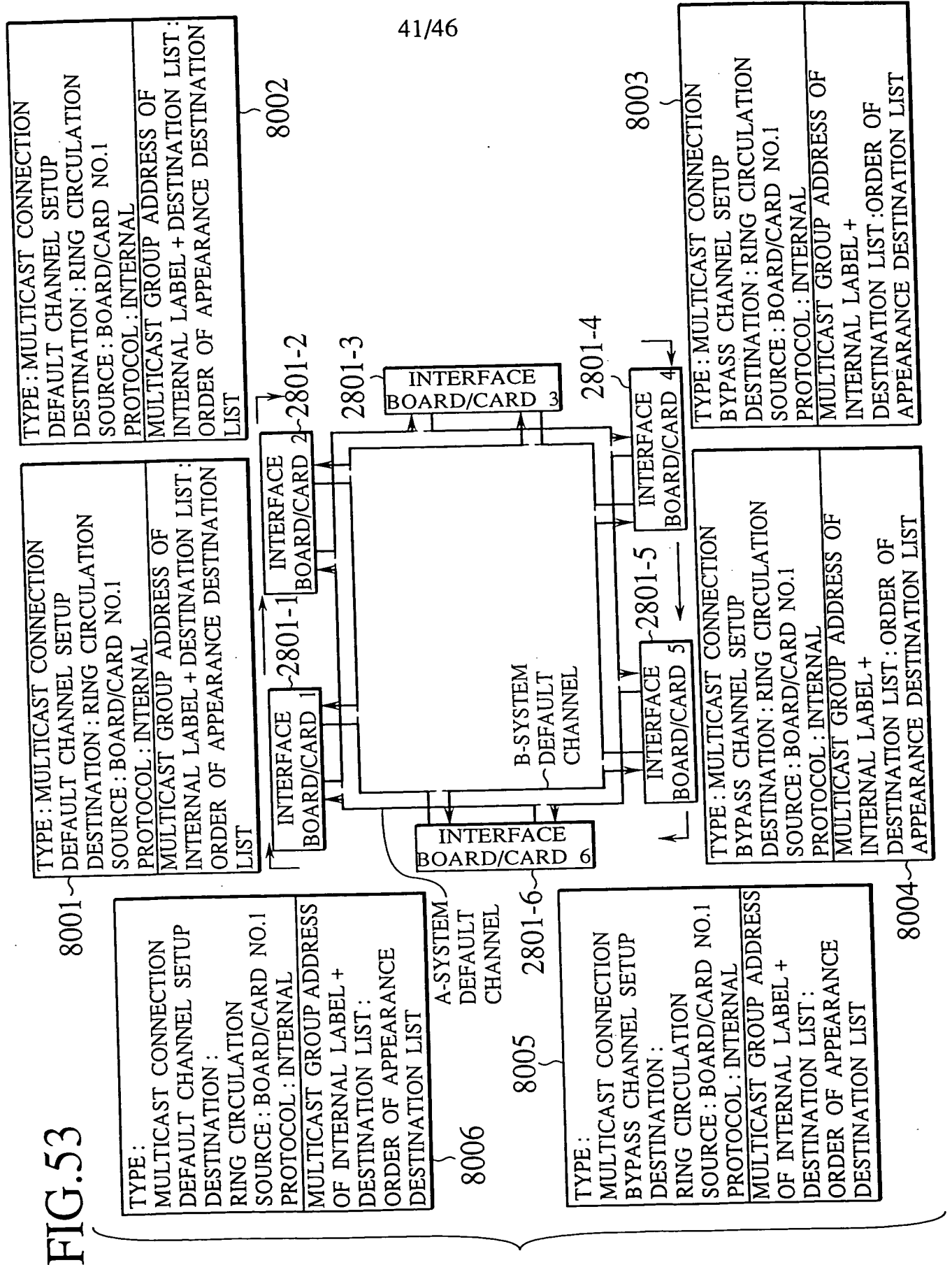


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FIG.52

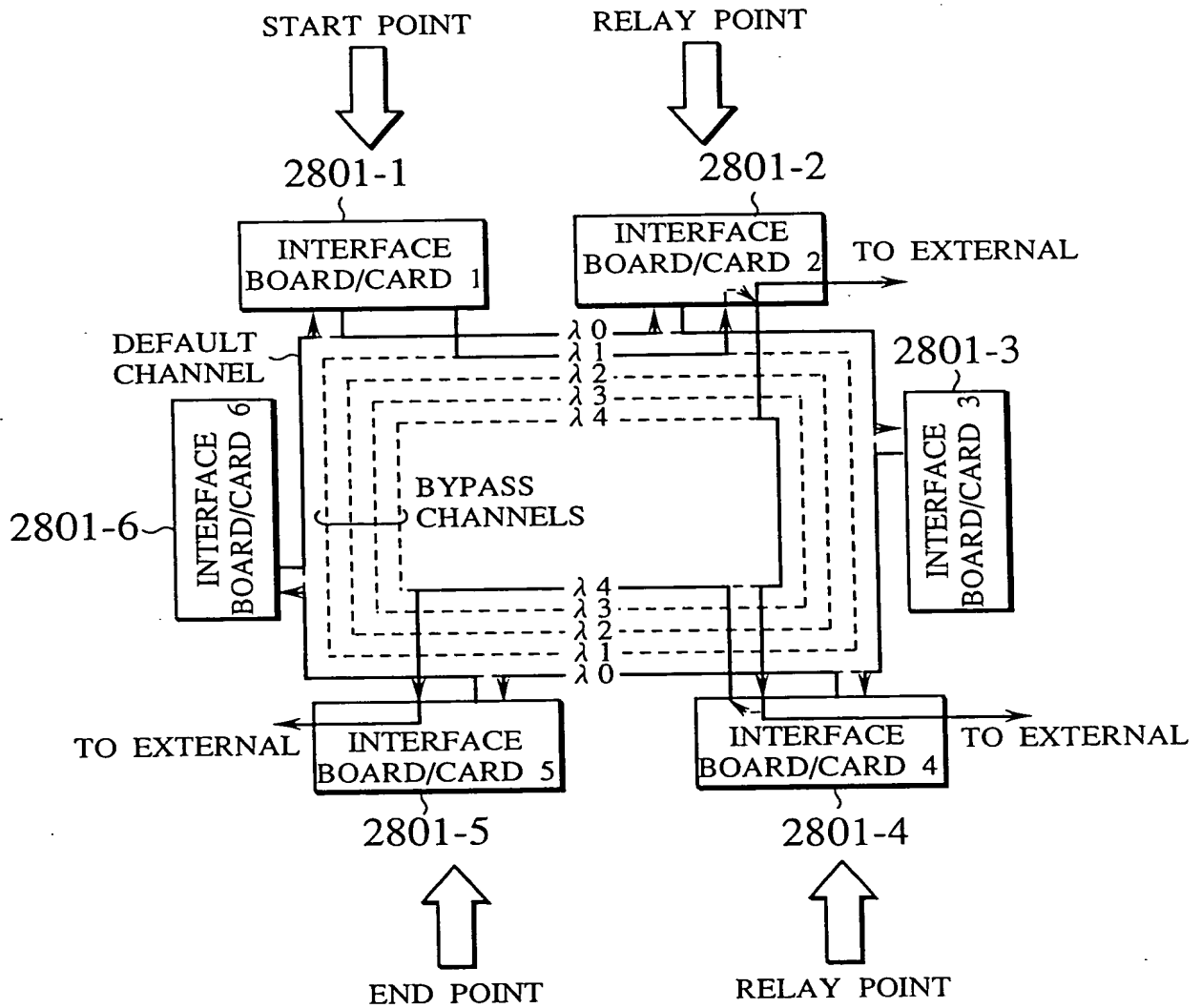


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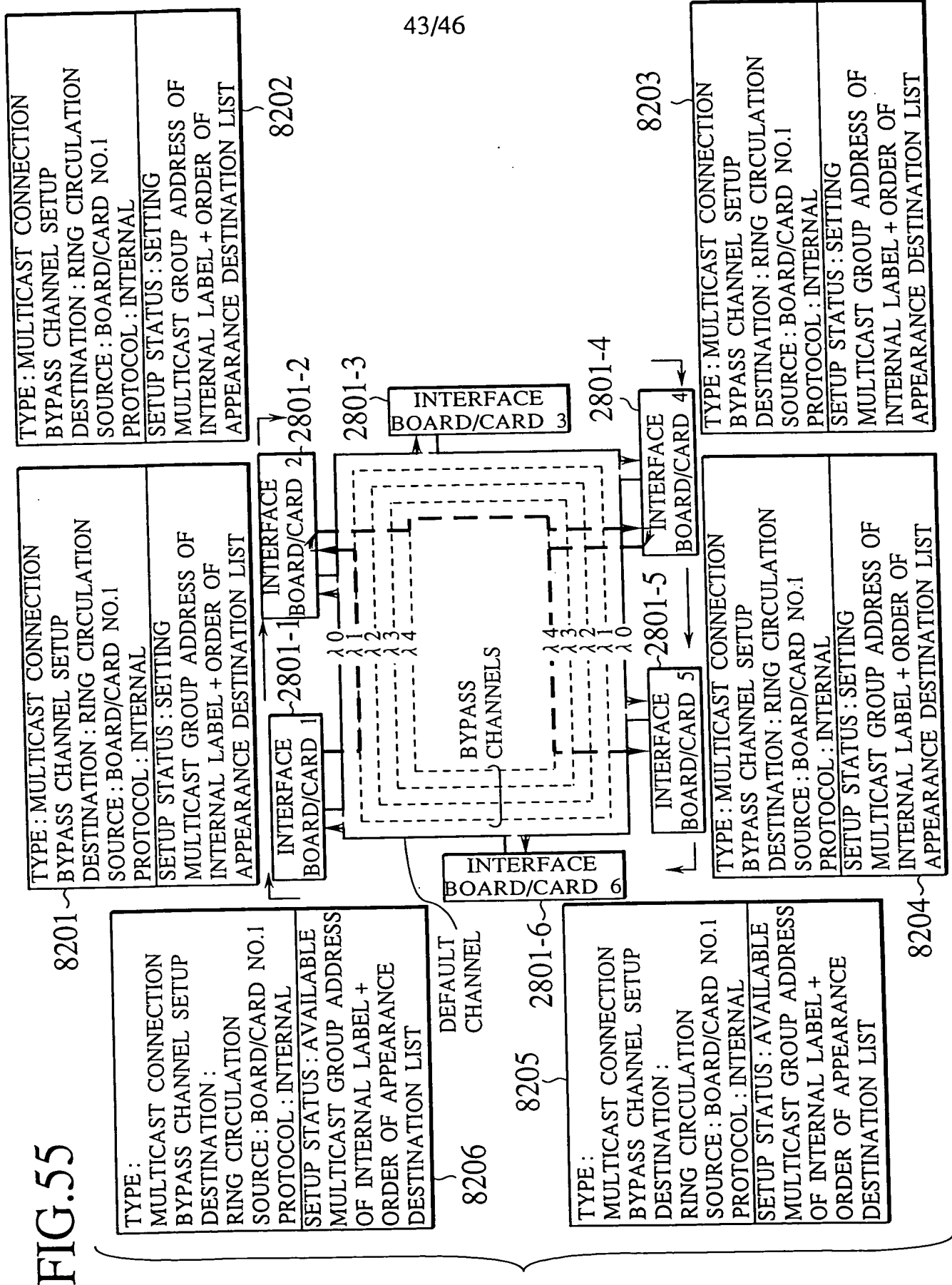


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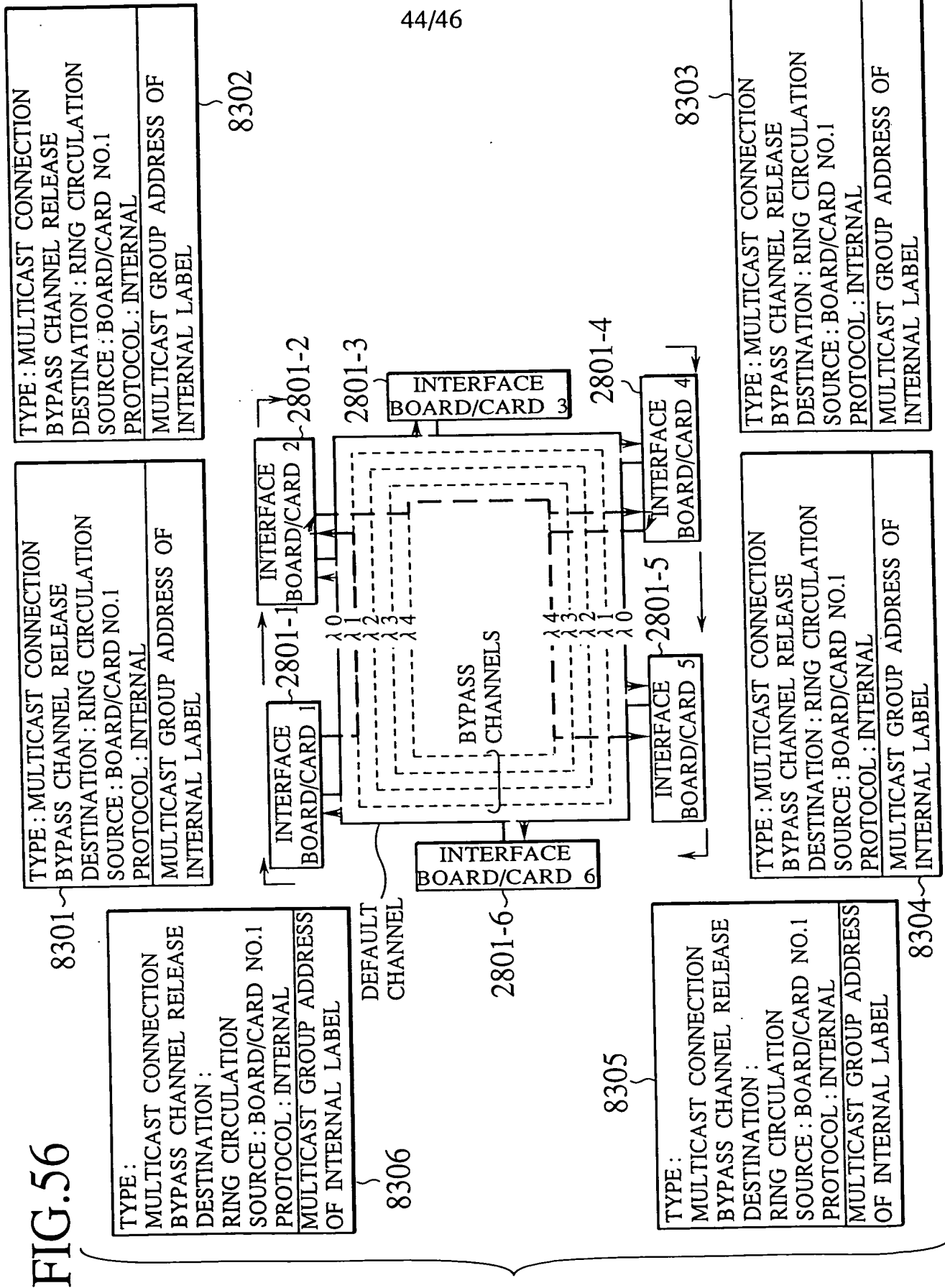
FIG.54



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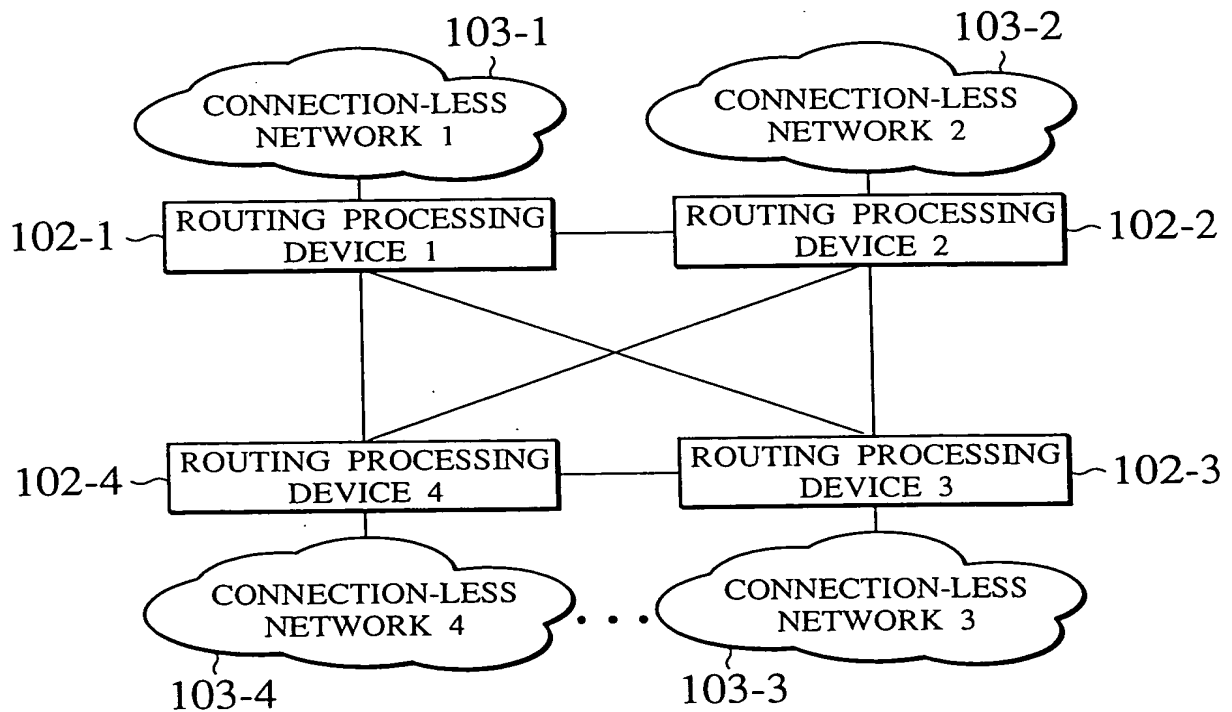


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FIG.57
PRIOR ART



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FIG.58

